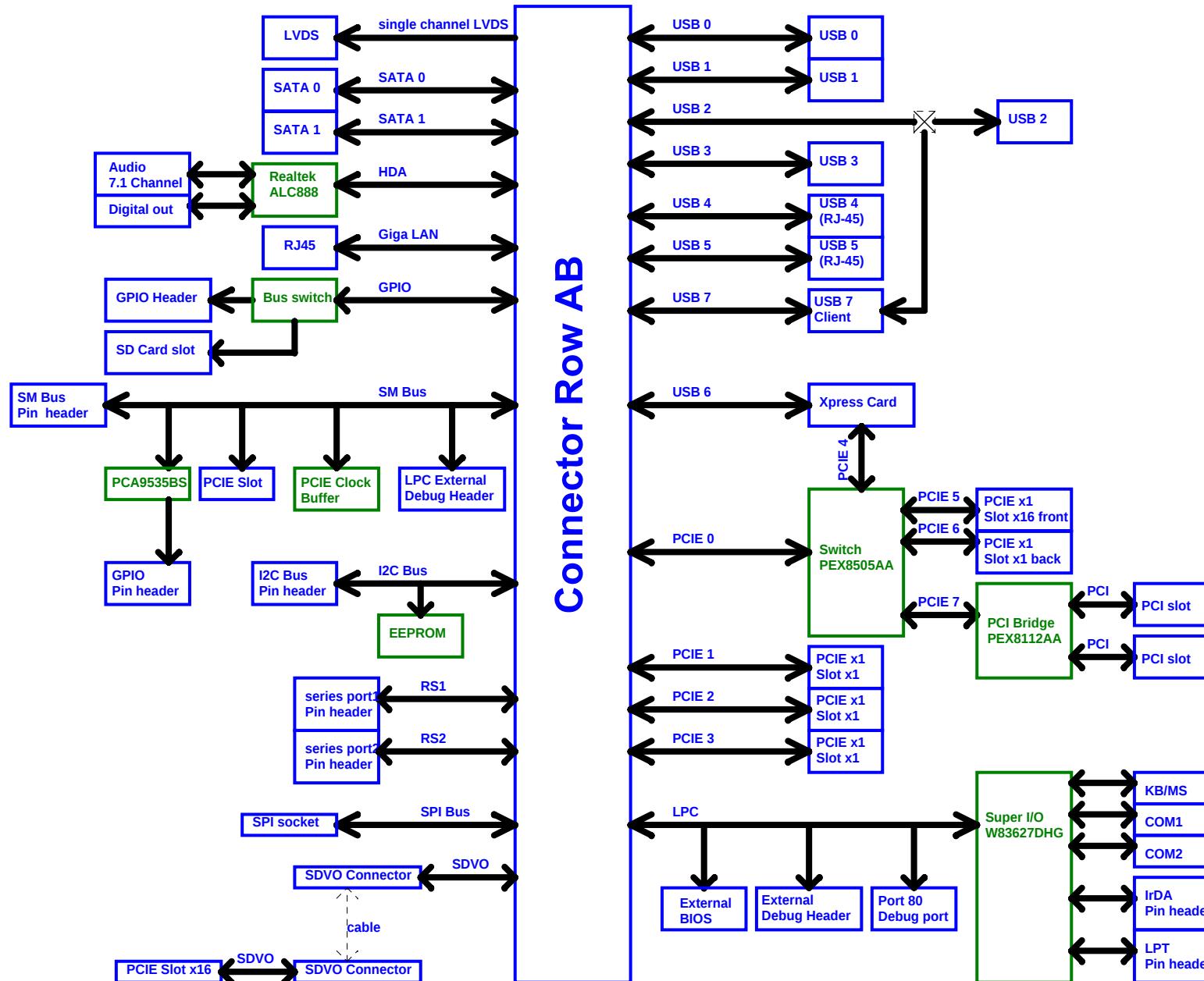


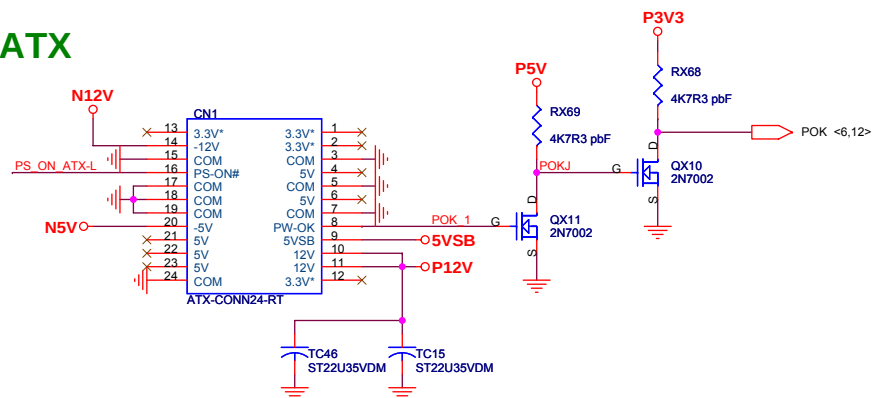
nanoX-BASE Rev. A3

51-77102-0A30

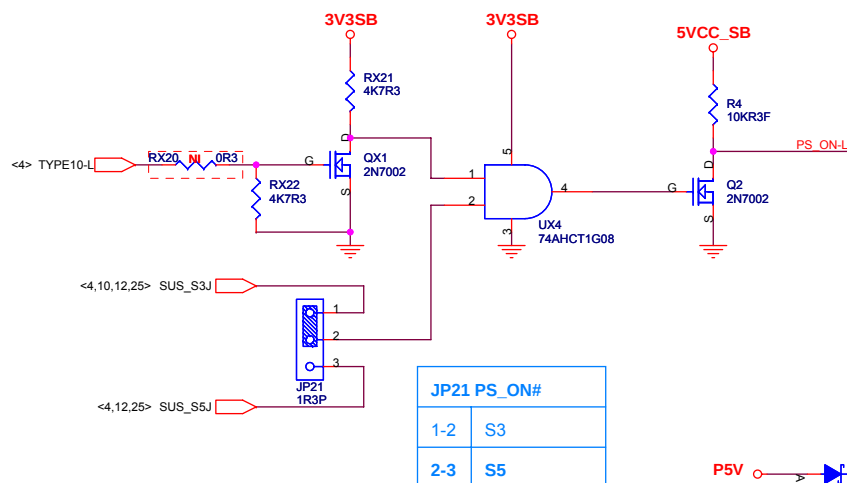
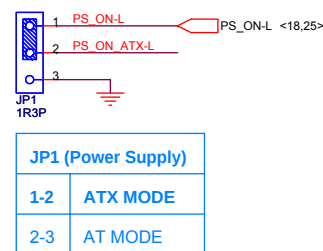
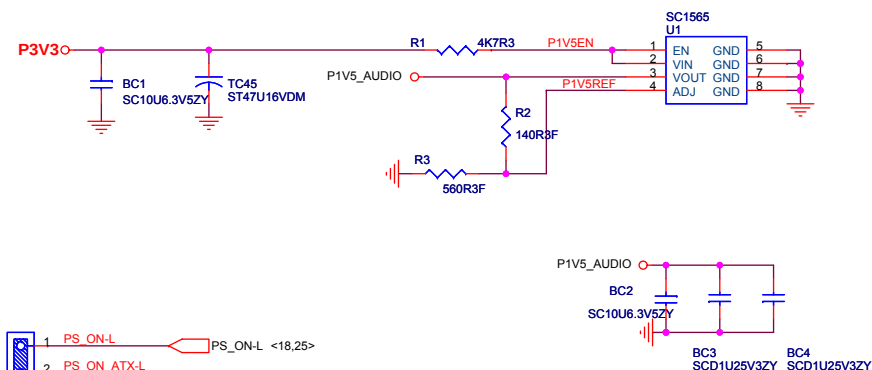
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ATX

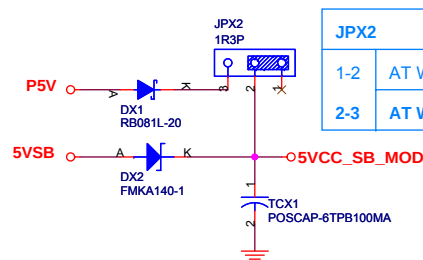
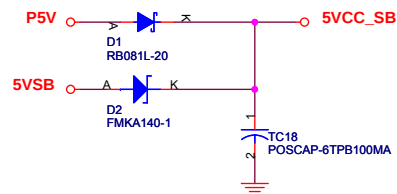
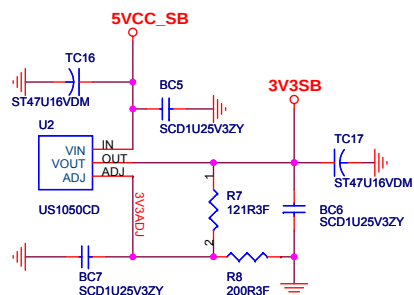


P3V3 to P1V5

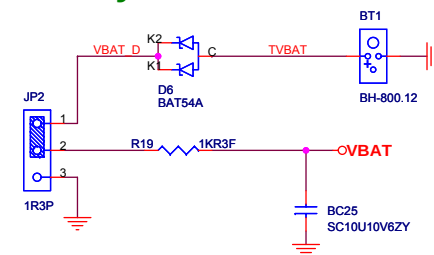


JP21 PS_ON#	
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2-3	S5

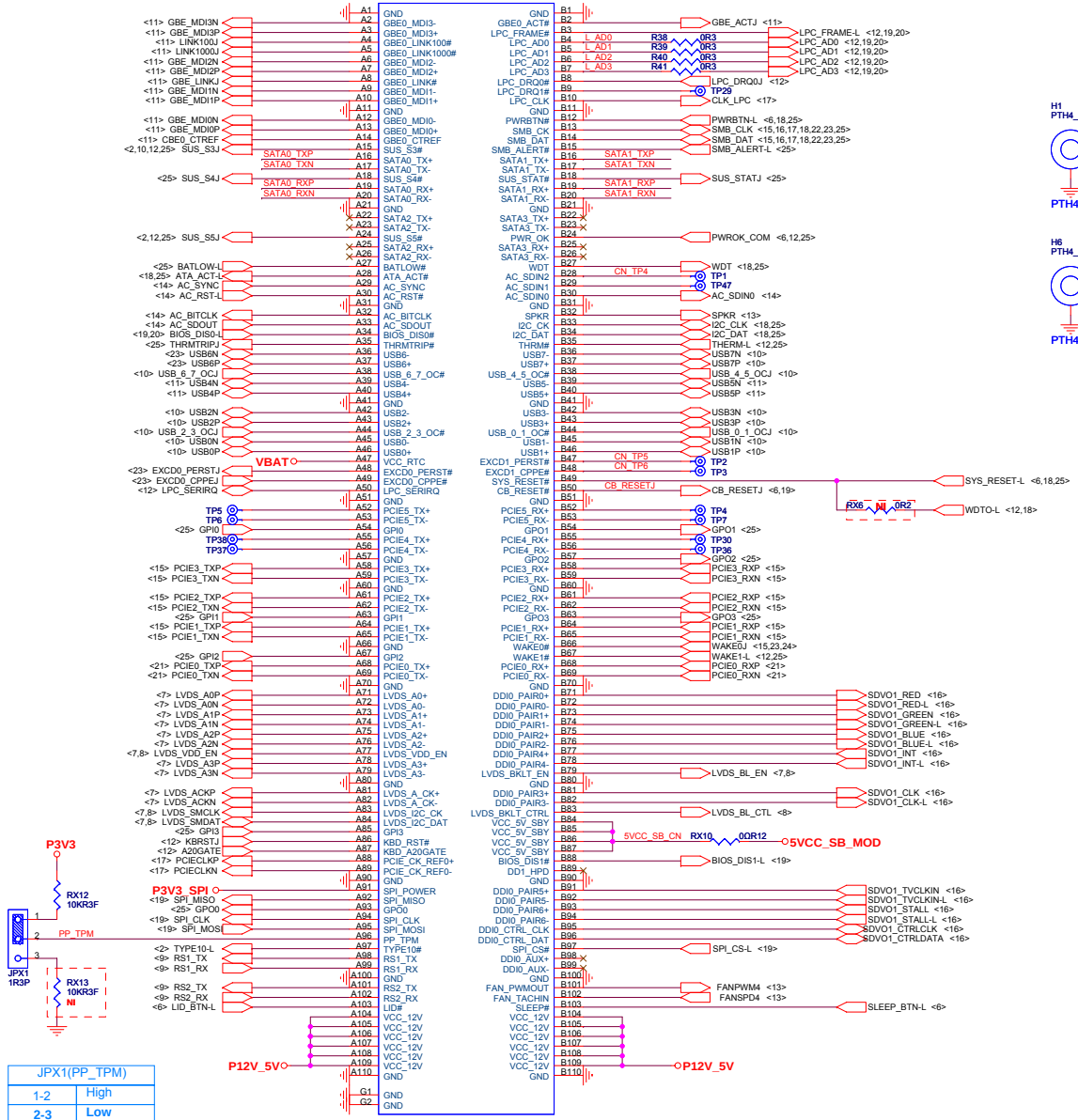
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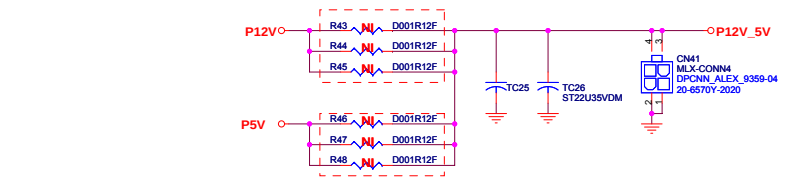
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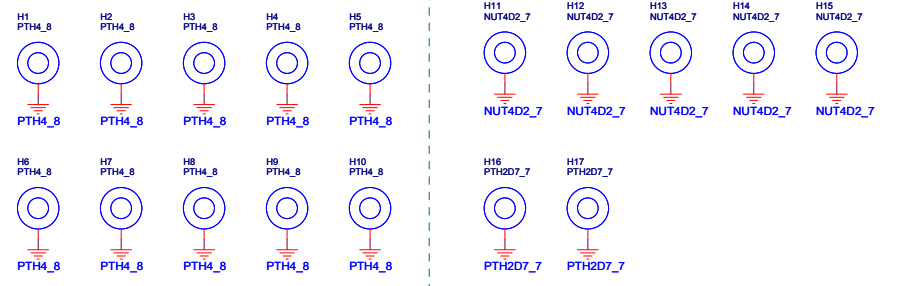
JP2	
1-2	Normal
2-3	Clear CMOS



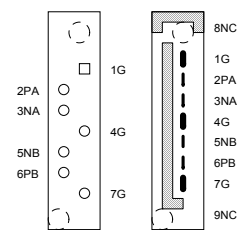
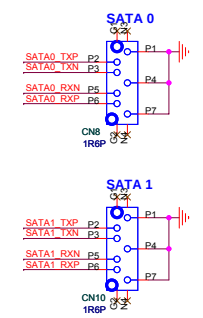
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1-2	High
2-3	Low



COM-Express Screw Hole

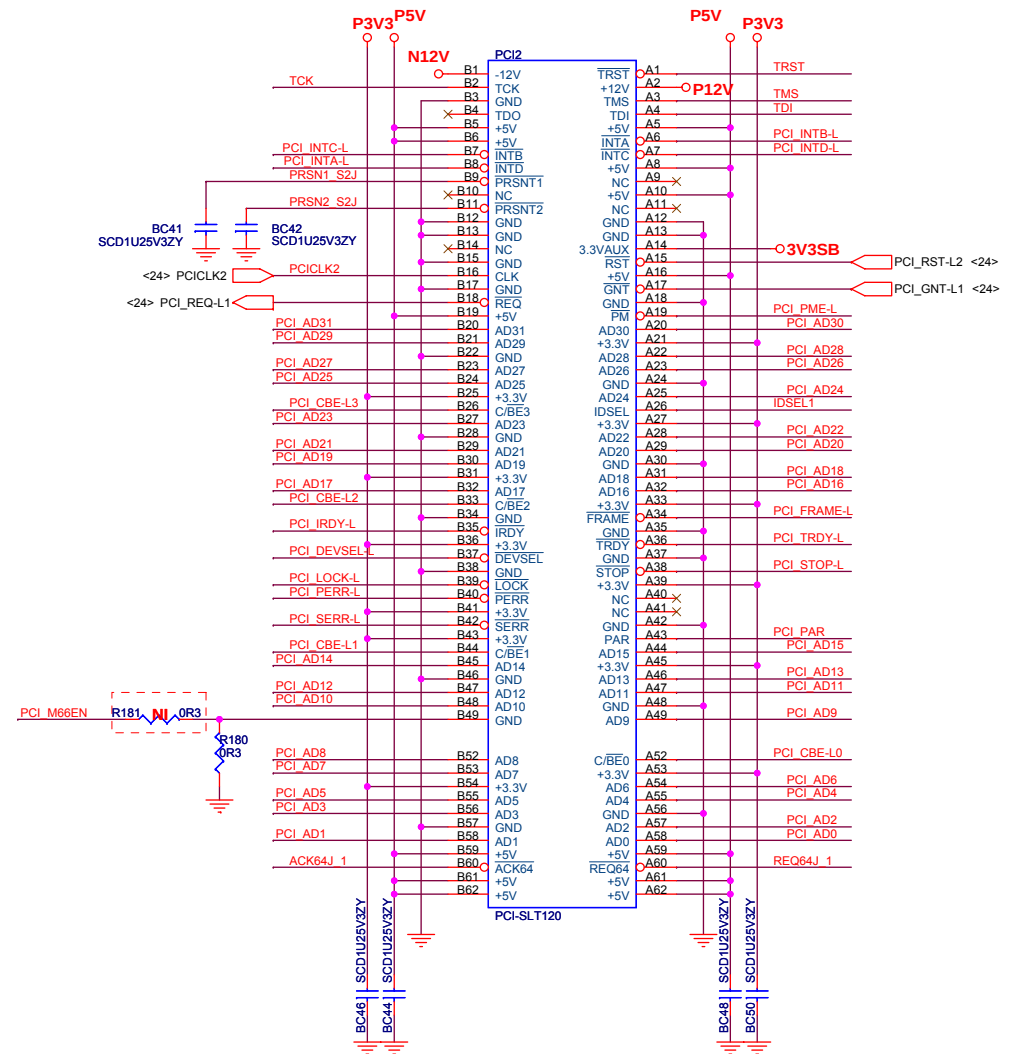
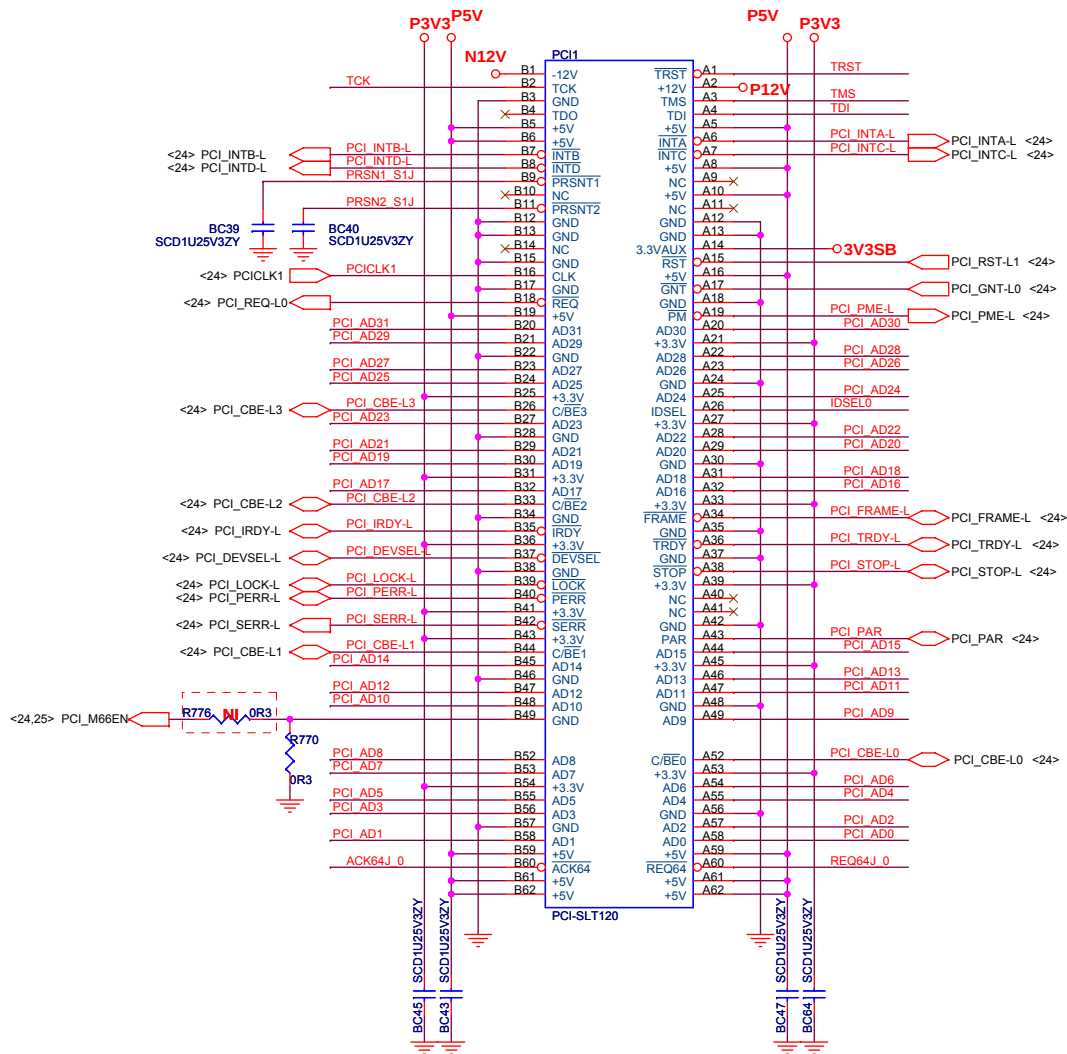


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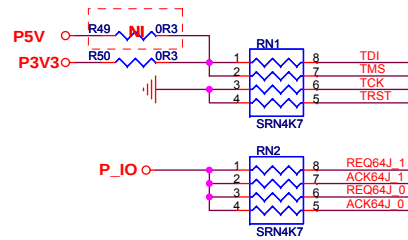
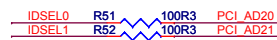
COM Express Connector		
Size	Document Number	Rev
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Carrier Board PCI Resource Allocation

	Slot / Device 0	Slot / Device 1	Slot / Device 2	Slot / Device 3
IDSEL	PCI_AD[20]	PCI_AD[21]	PCI_AD[22]	PCI_AD[23]
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INTB#	PCI_IRQ[B]#	PCI_IRQ[C]#	PCI_IRQ[D]#	PCI_IRQ[A]#
INTC#	PCI_IRQ[C]#	PCI_IRQ[D]#	PCI_IRQ[A]#	PCI_IRQ[B]#
INTD#	PCI_IRQ[D]#	PCI_IRQ[A]#	PCI_IRQ[B]#	PCI_IRQ[C]#

<24> PCI_AD[0:31]



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Title
PCI Slot

Size
A3

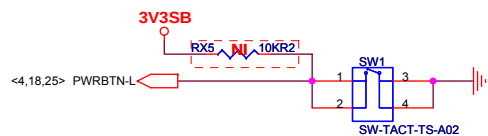
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nanoX-BASE

Date
Tuesday, July 27, 2010

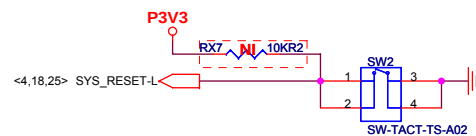
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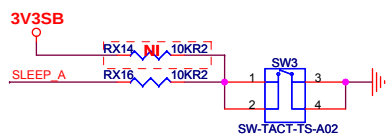
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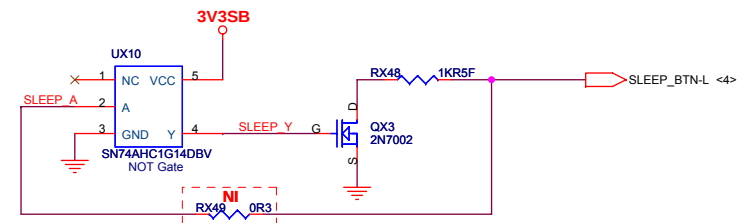
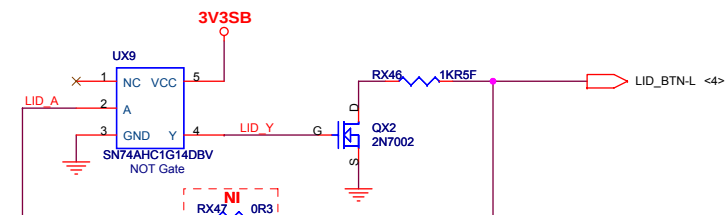
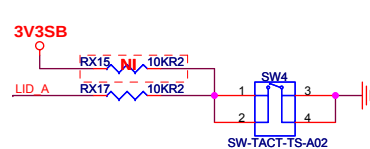
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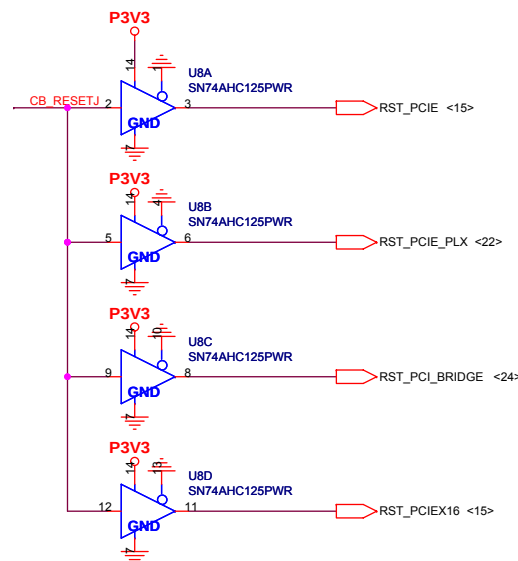
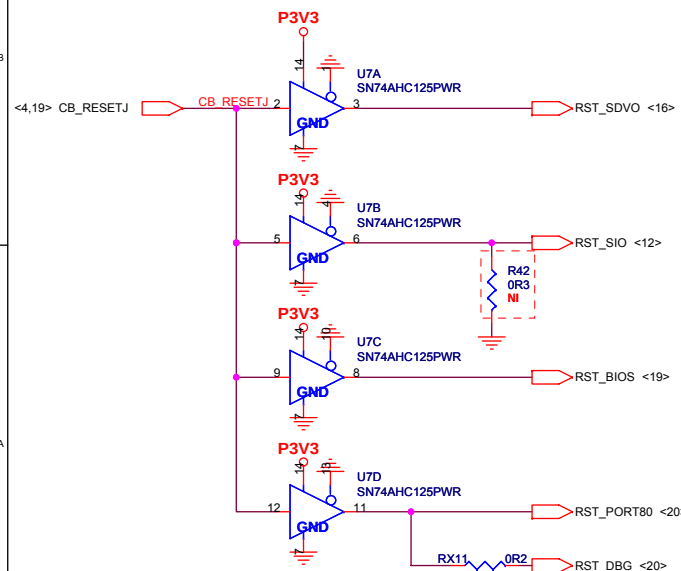
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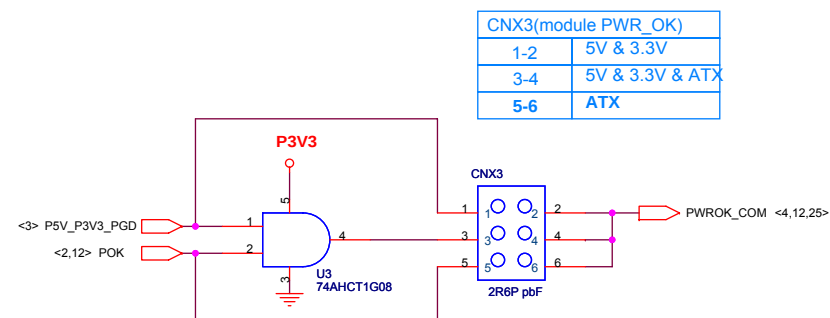
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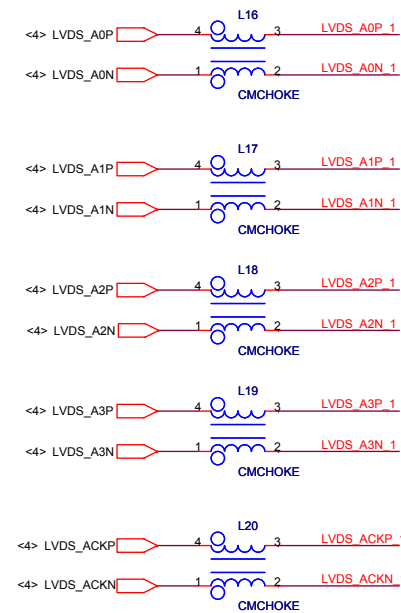
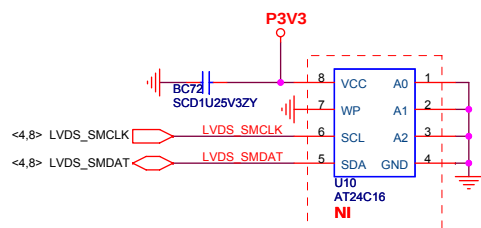
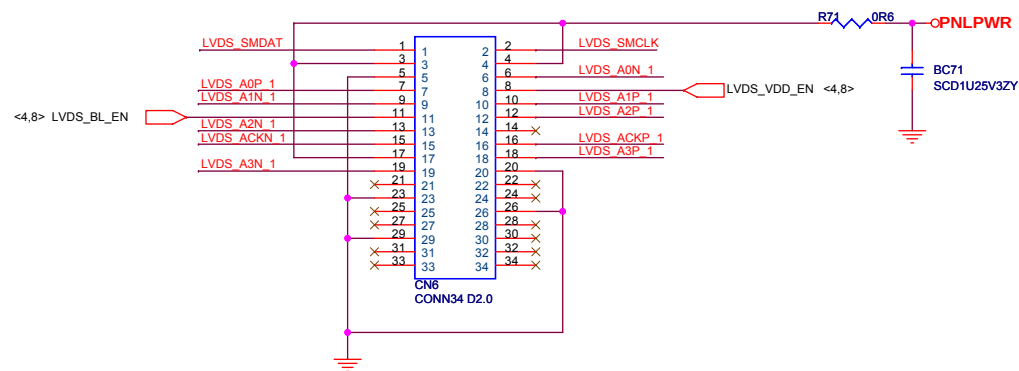
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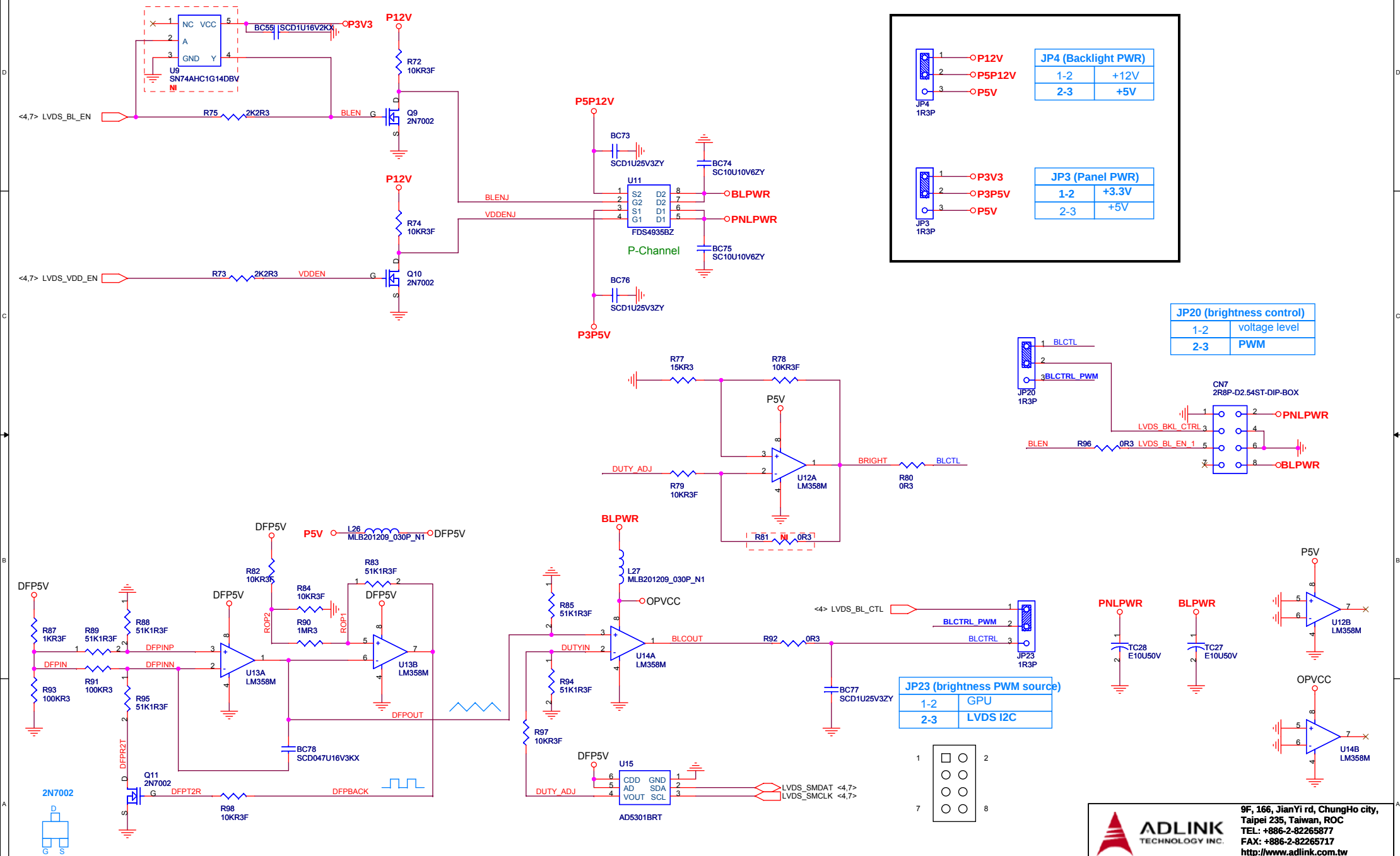
POWER OK



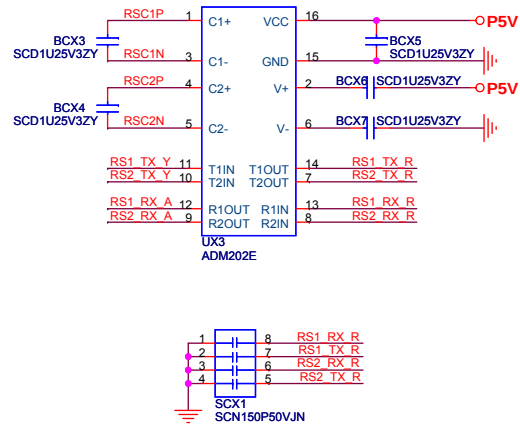
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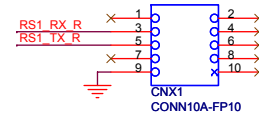
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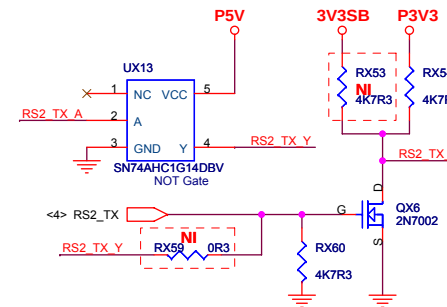
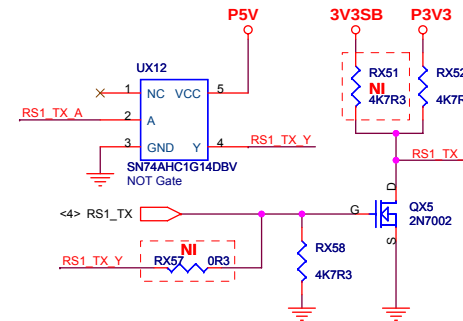
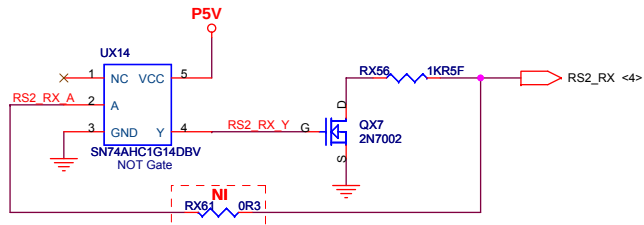
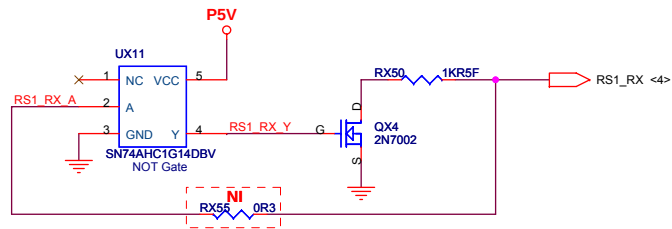
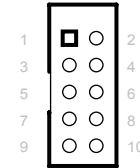
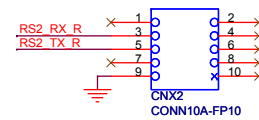
RS232 from module

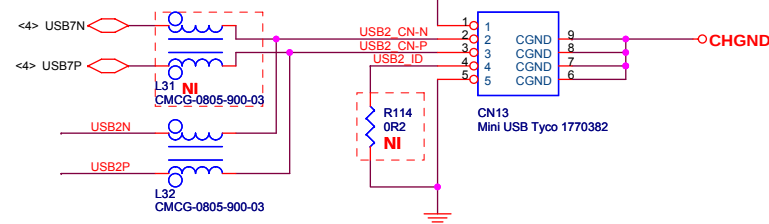
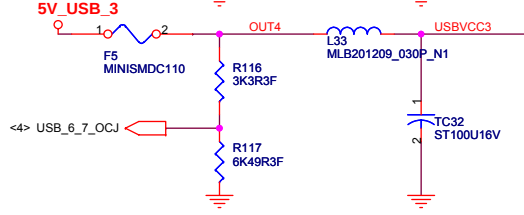
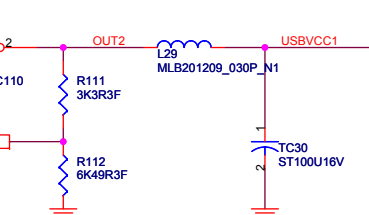
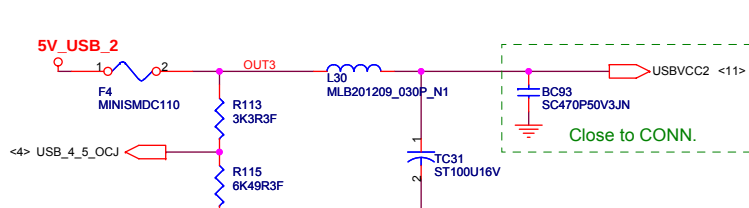
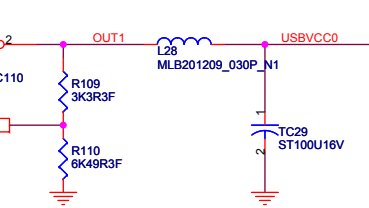
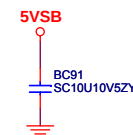
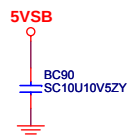
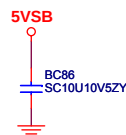
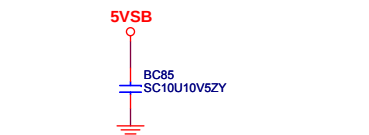
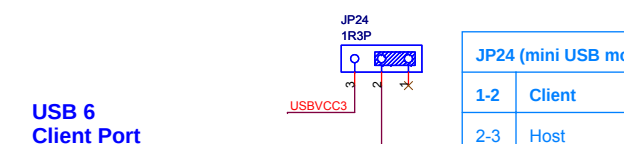
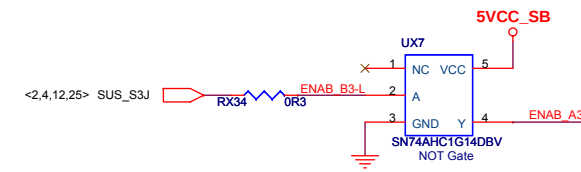
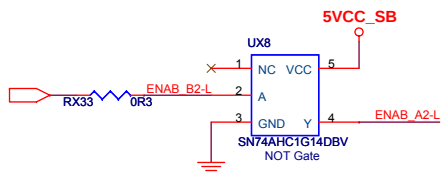
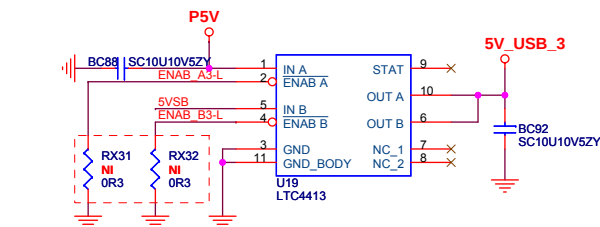
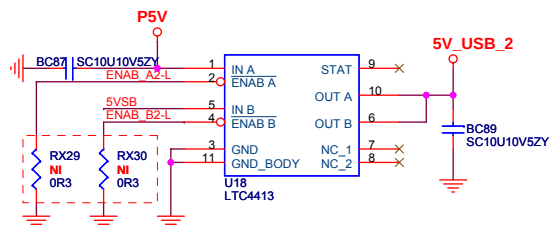
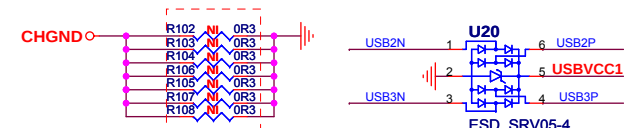
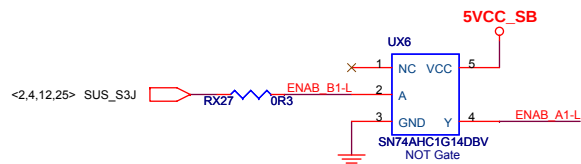
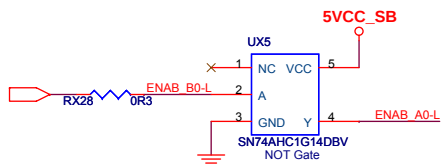
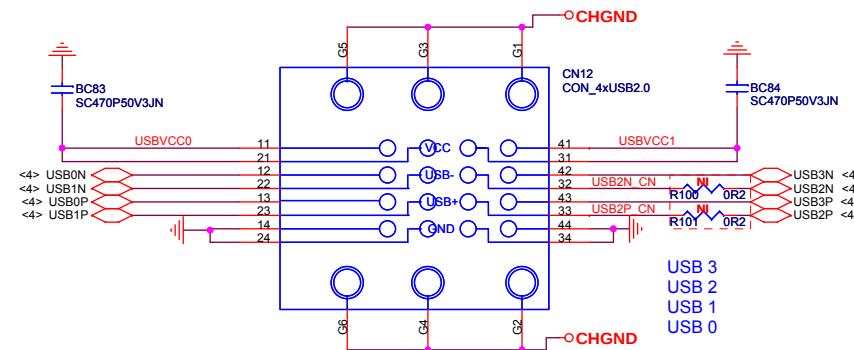
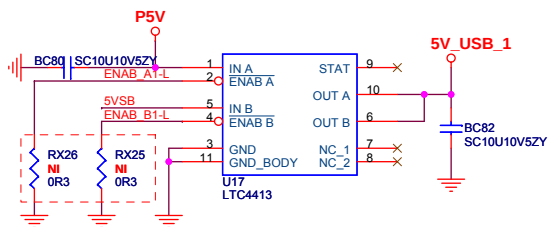
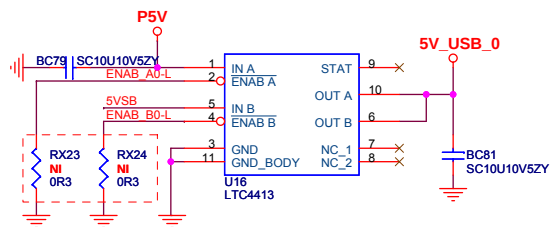


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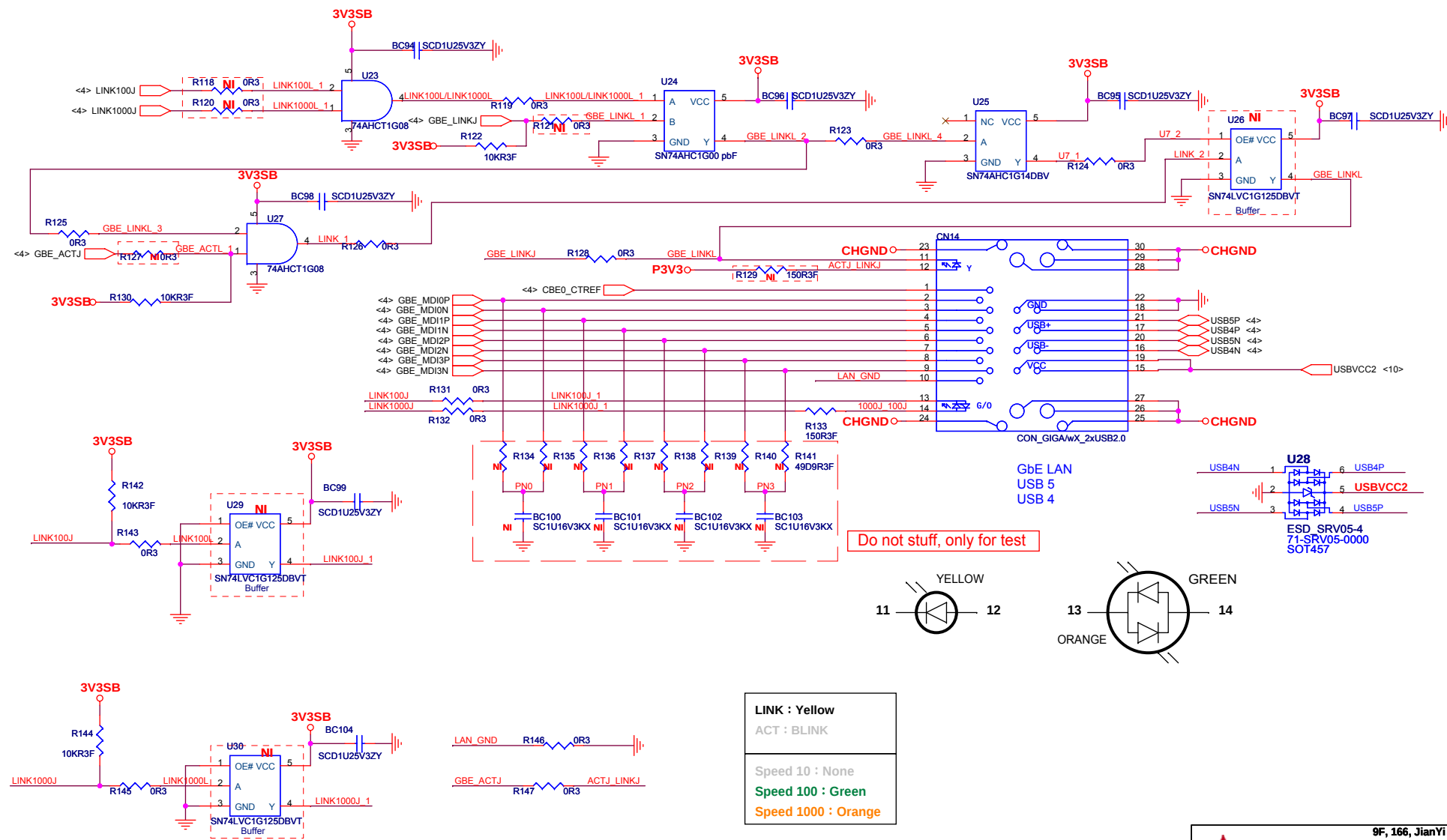
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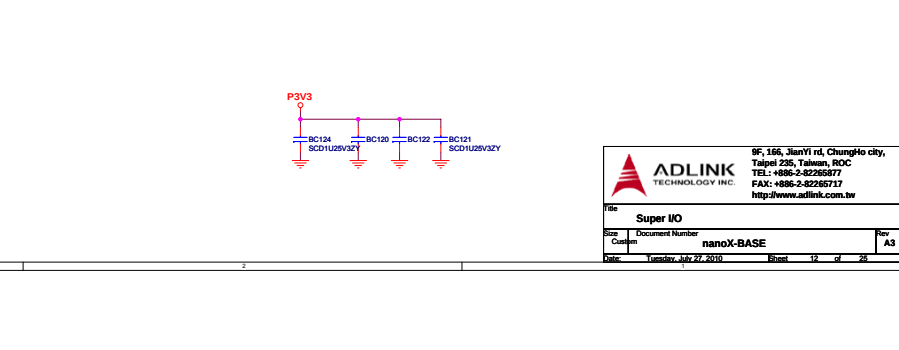
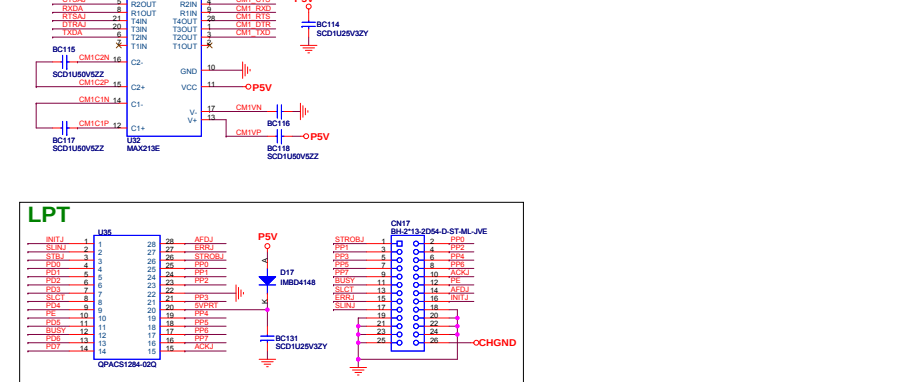
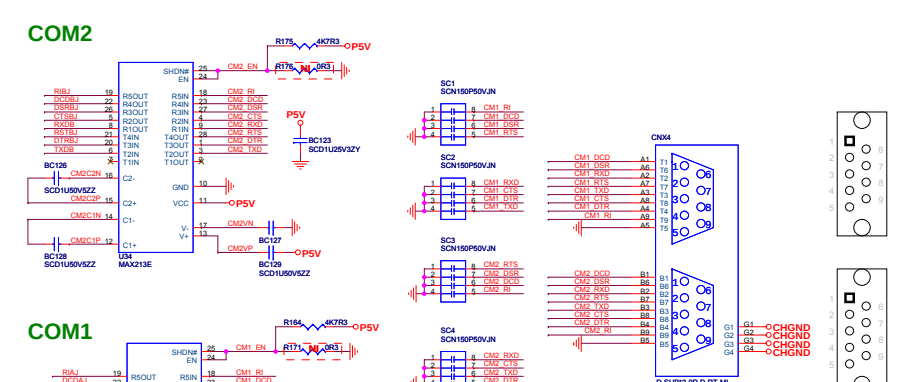
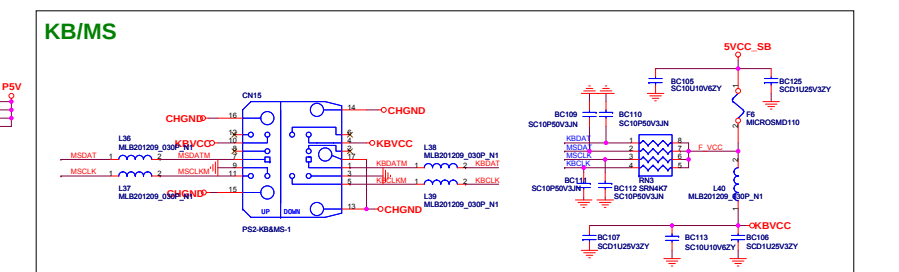
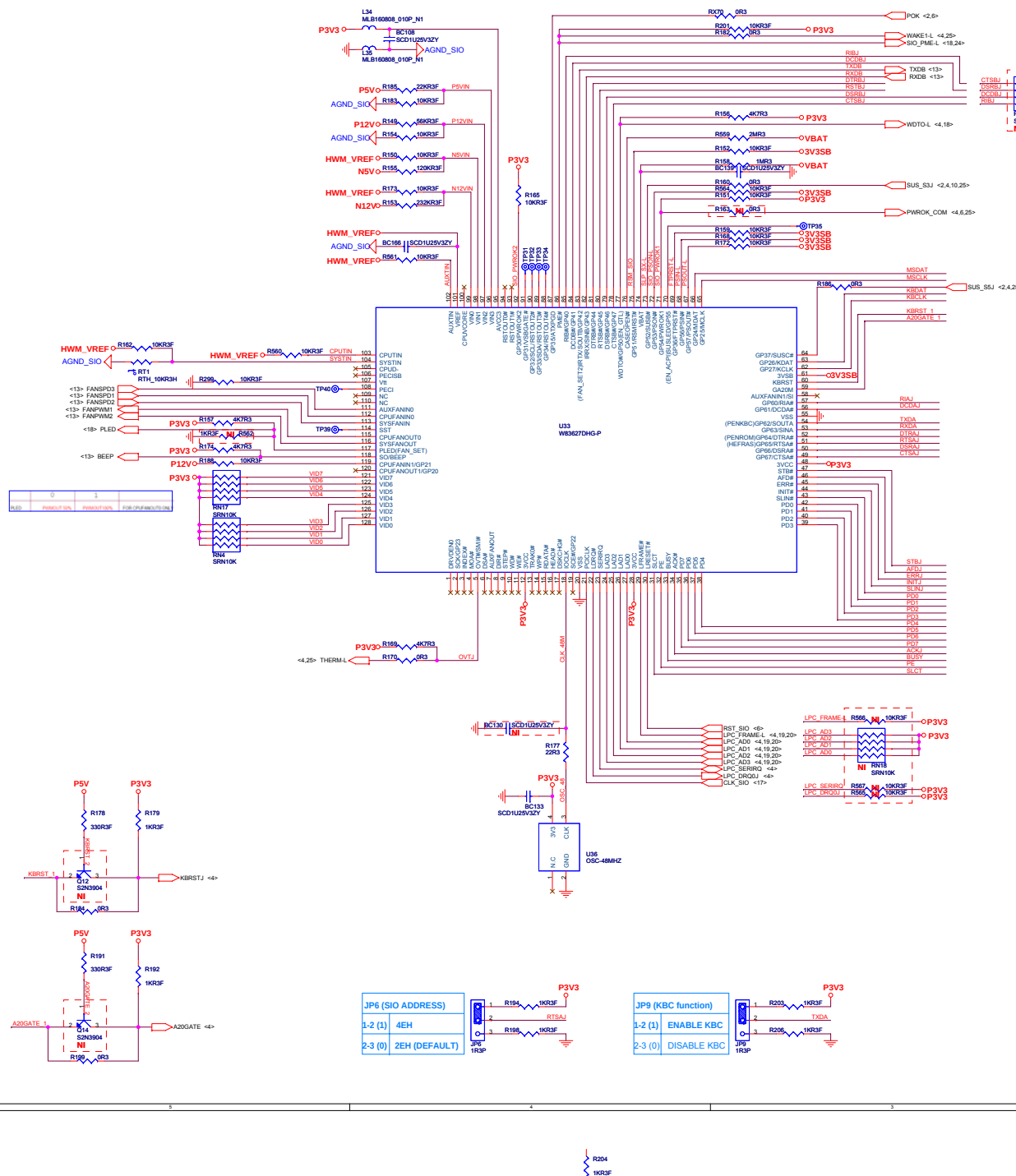




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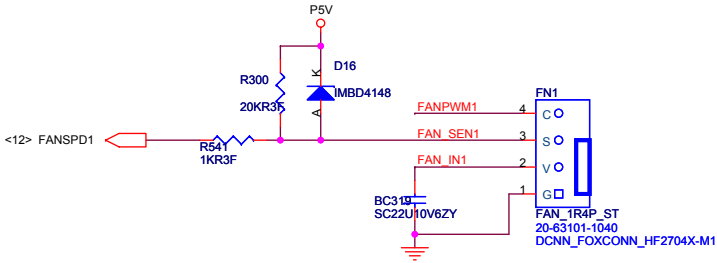
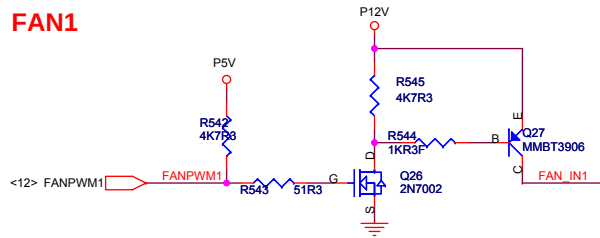


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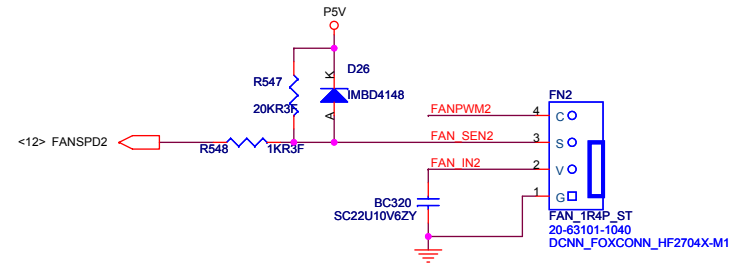
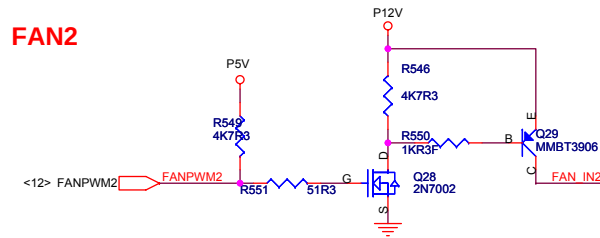
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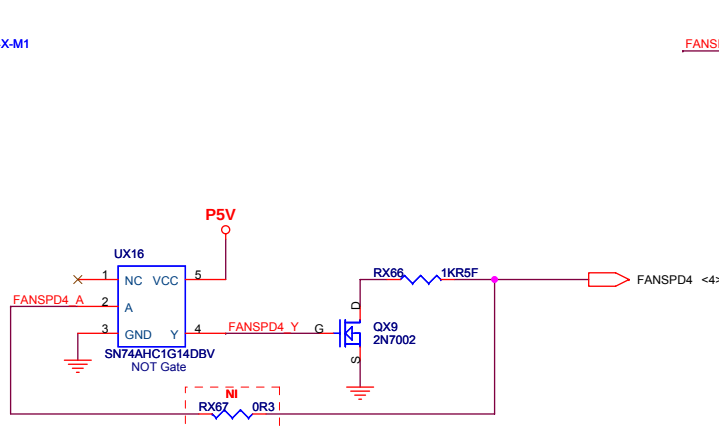
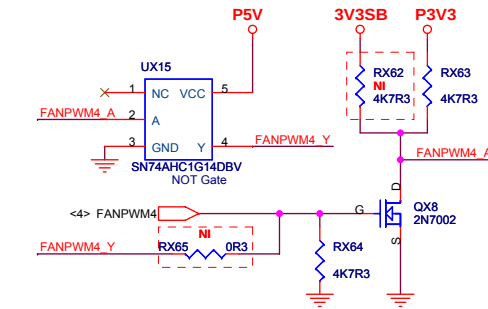
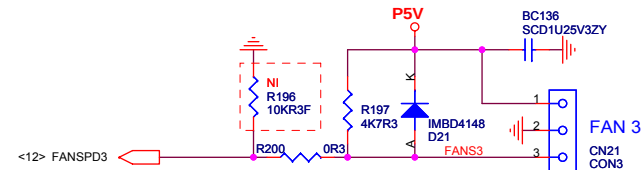
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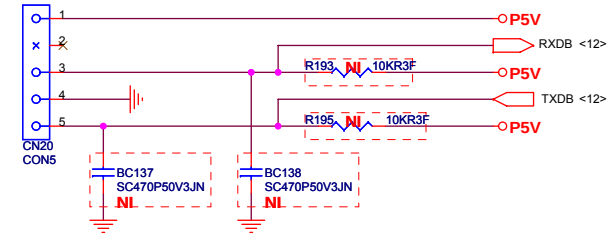
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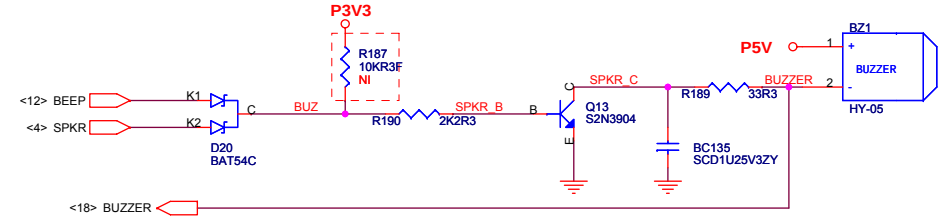
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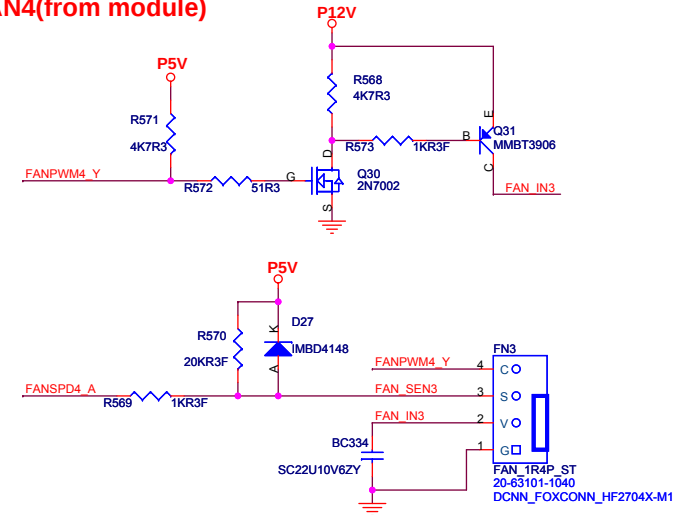
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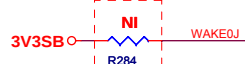
BUZZER



FAN4(from module)



The WAKE# signal is an open drain, active low signal that is driven low by a PCI Express component to reactivate the PCI Express slot's main power rails and reference clocks.

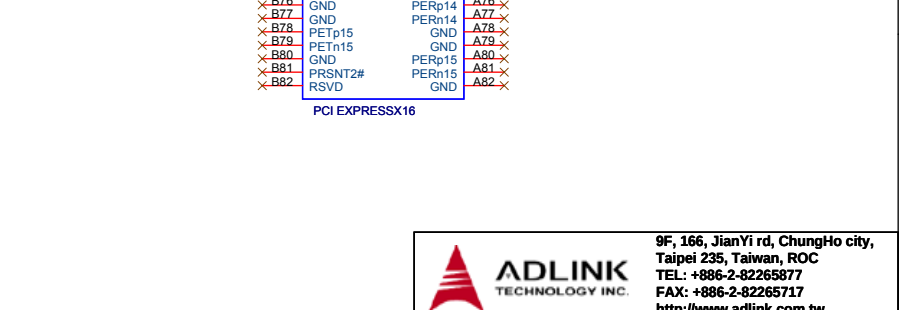
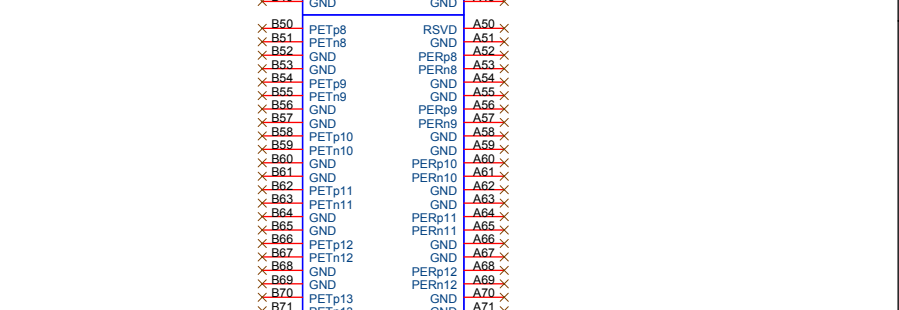
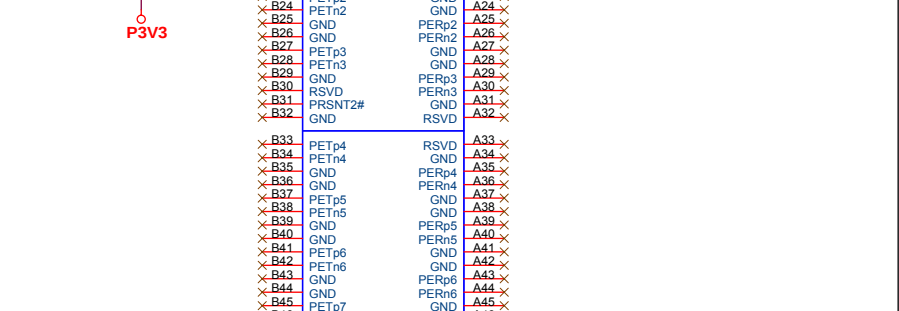
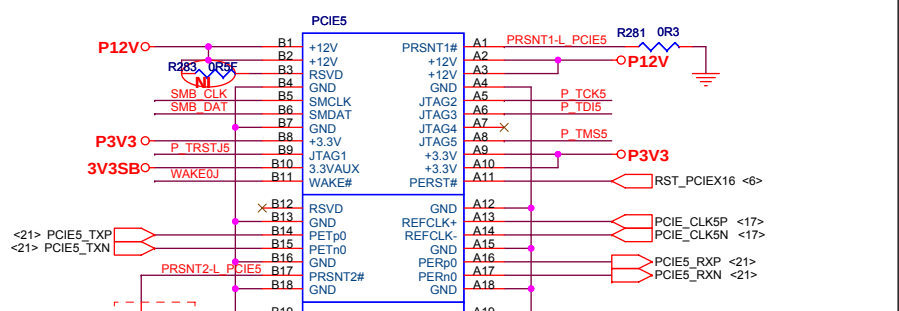
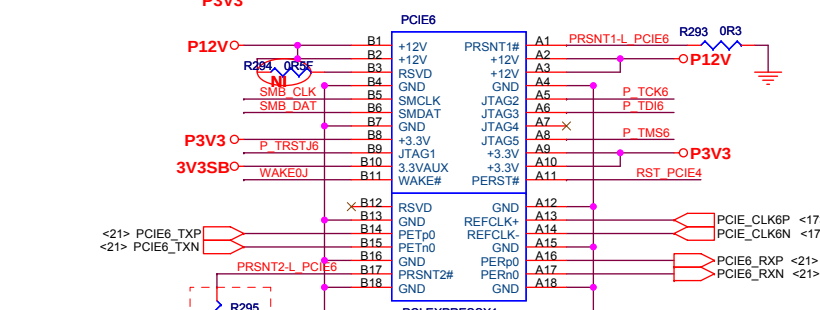
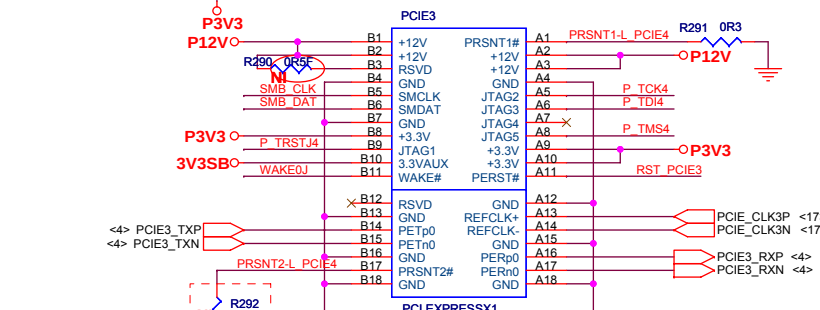
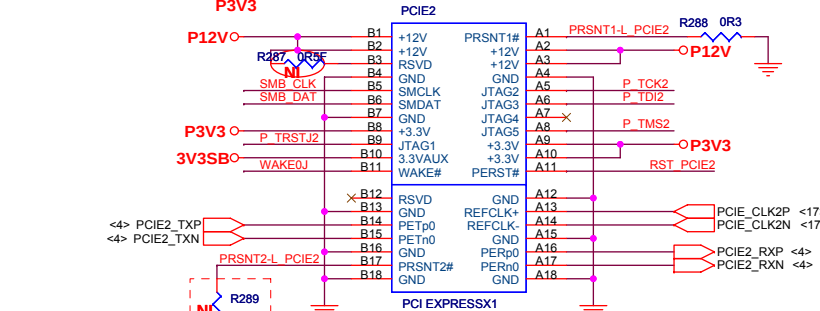
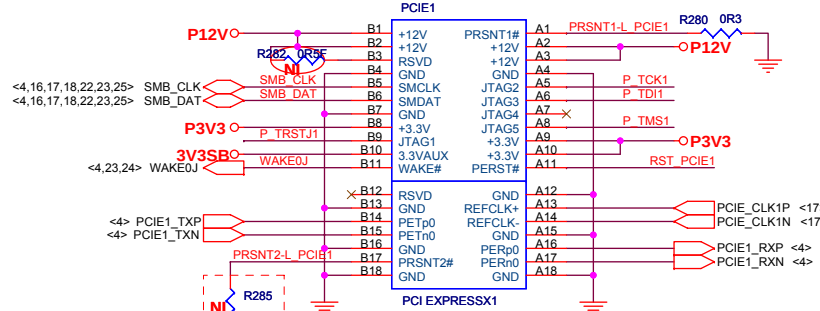
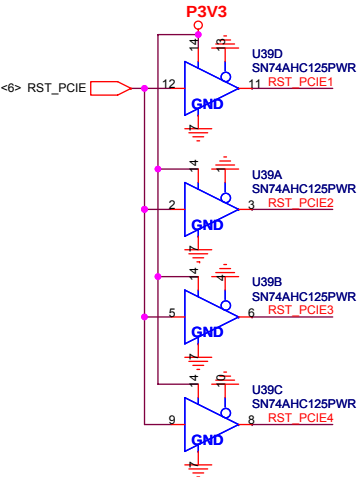
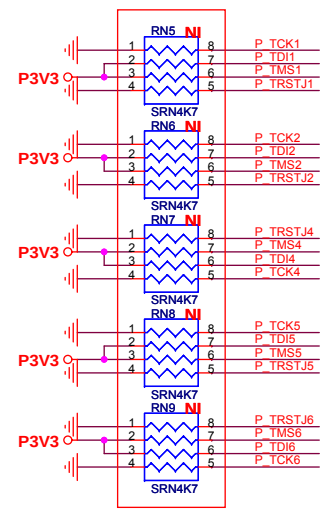
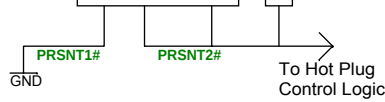


PCI Express Hot-Plug controller detects

PULL-UP

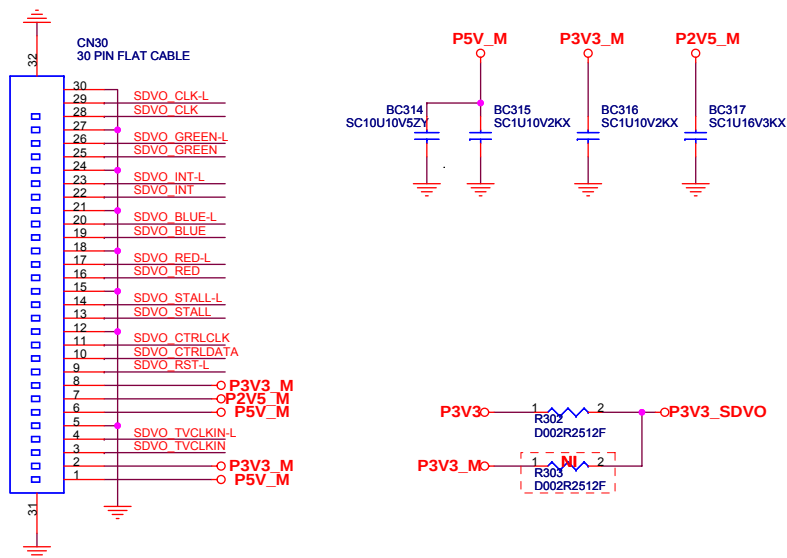
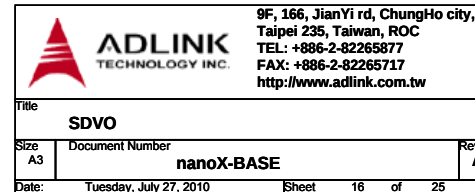
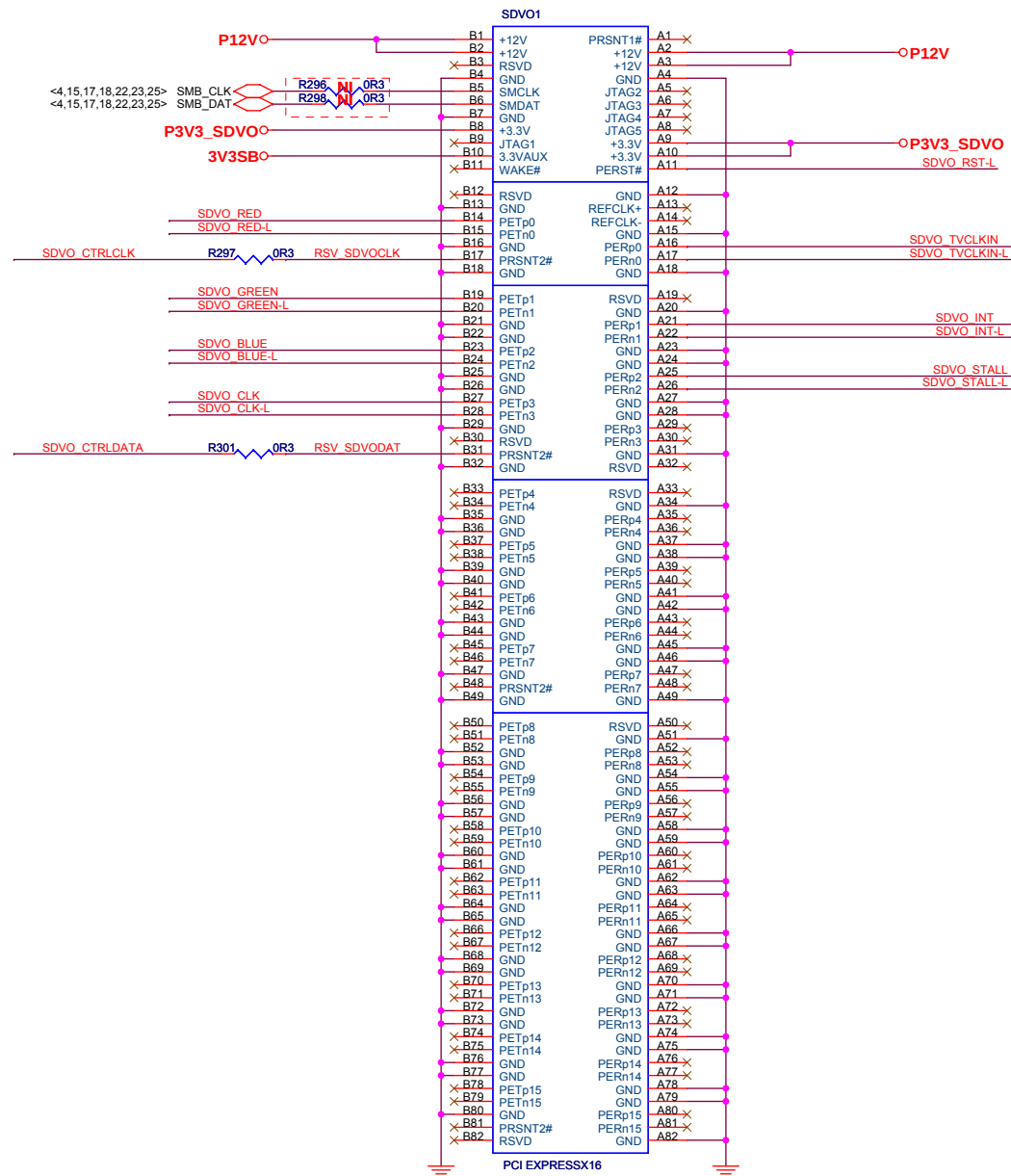
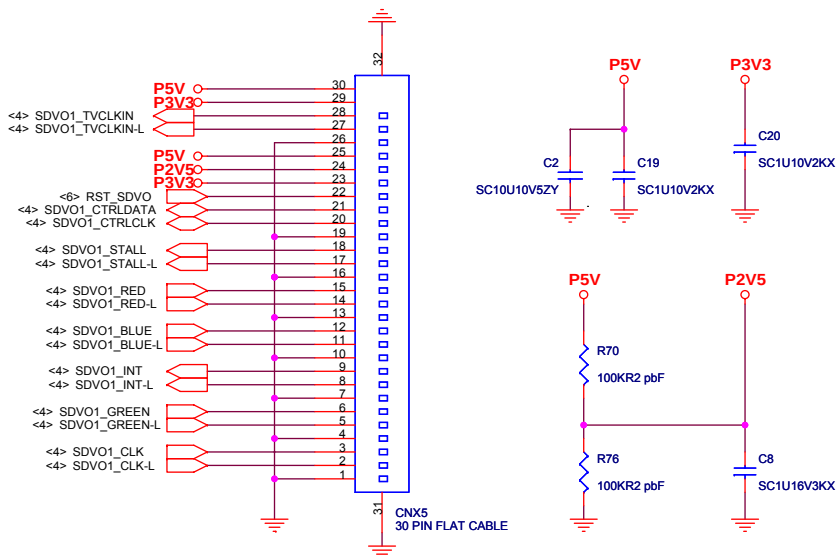
R

PCIE CONN



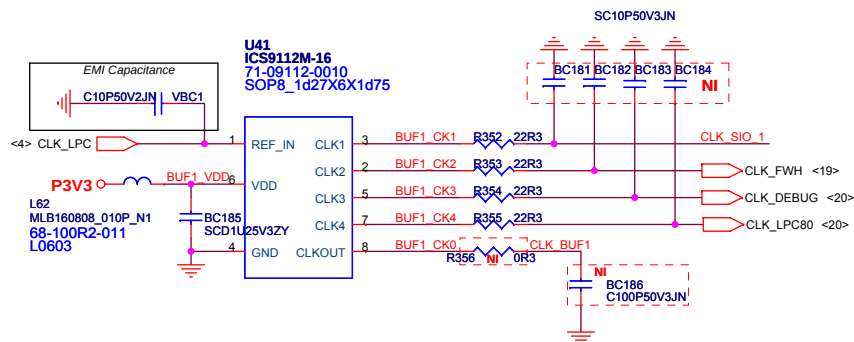
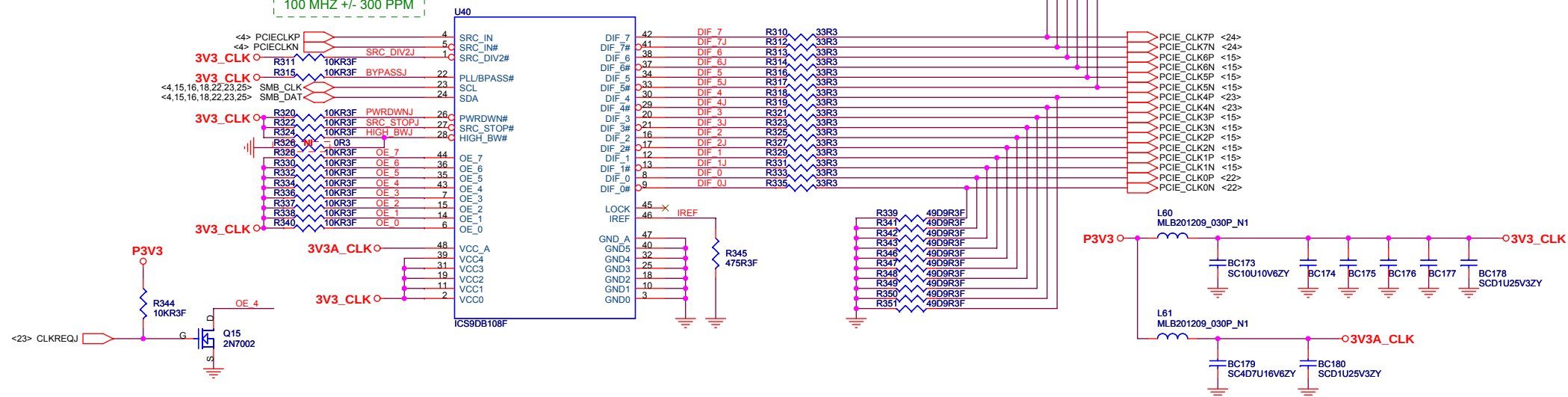
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to SDVO slot

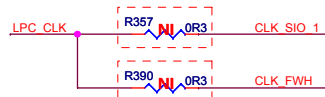
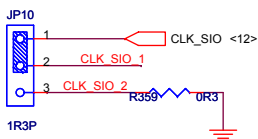
**from module**

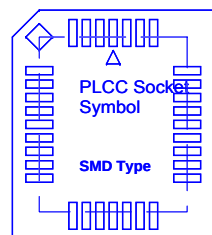
The REFCLK+/REFCLK- signals are used to assist the synchronization of the card's PCI Express interface timing circuits.

100 MHZ +/- 300 PPM

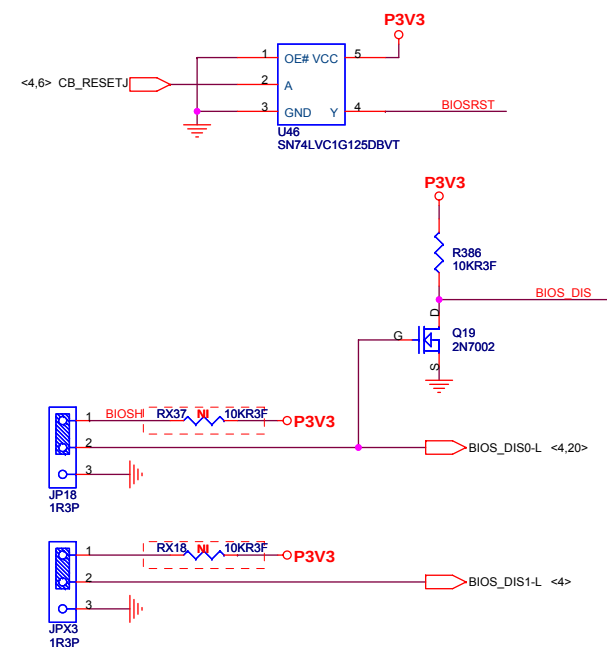
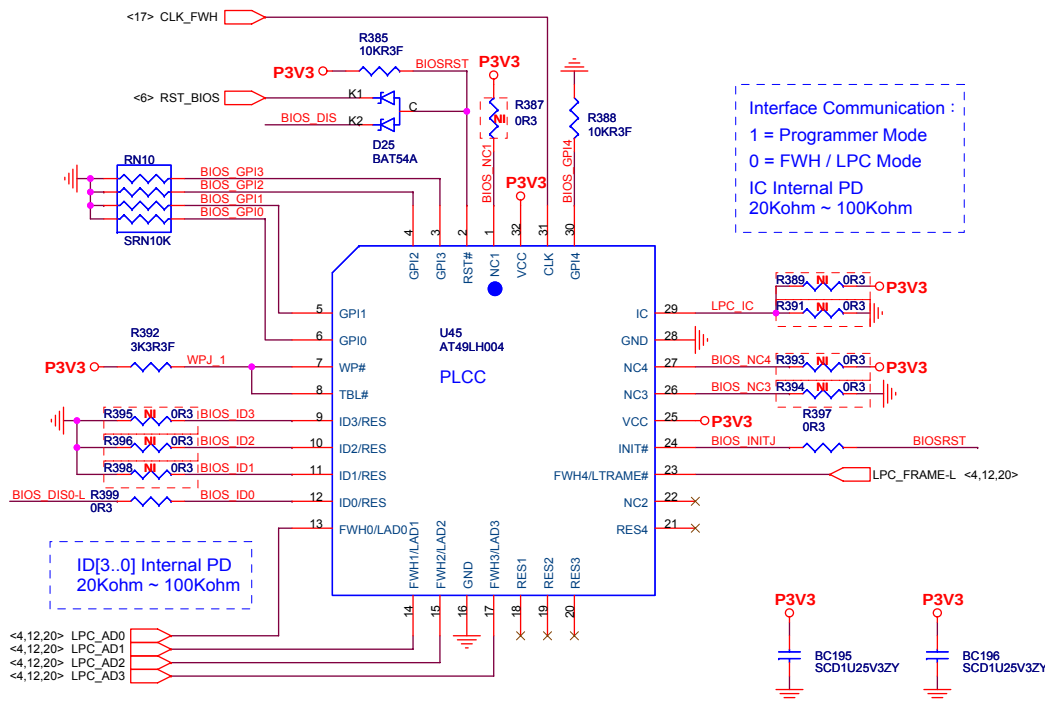


JP10 (Super IO)	
1-2	Enable
2-3	Disable





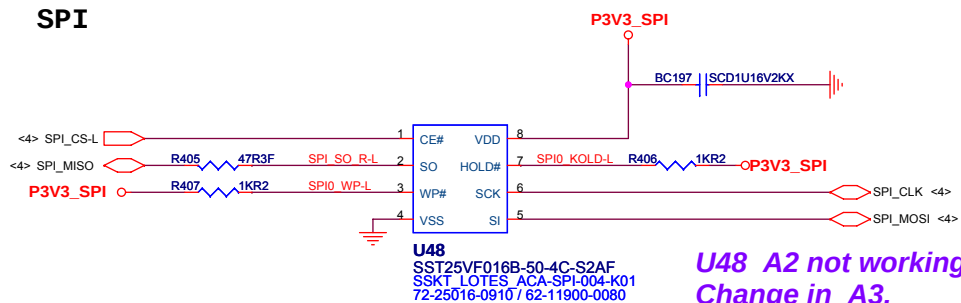
SKT1
SSKT32



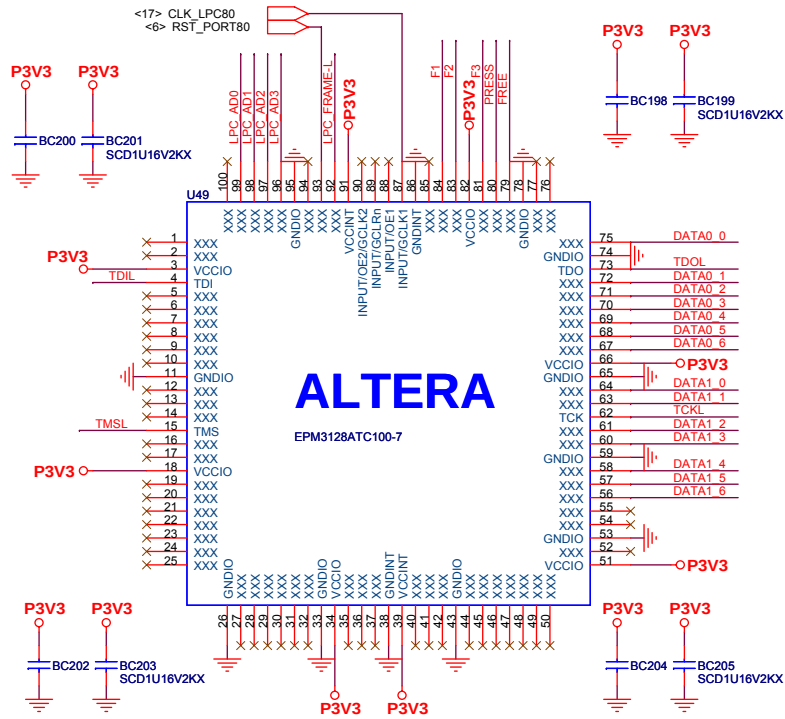
JPX3 (BIOS_DIS1#)	JP18 (BIOS_DIS0#)	
1-2	1-2	Module SPI
1-2	2-3	Carrier FWH
2-3	1-2	Carrier SPI0
2-3	2-3	Carrier SPI1

A2 not working
A2 not working

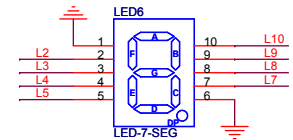
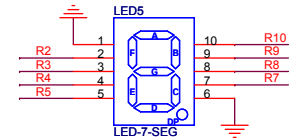
SPI



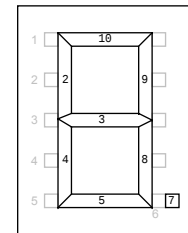
LPC Debug Port



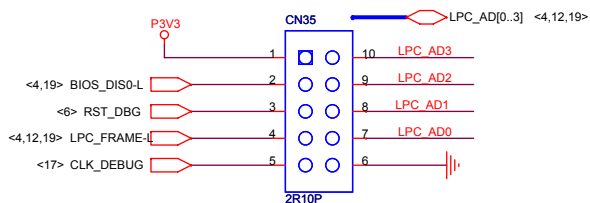
NOTE :Placement

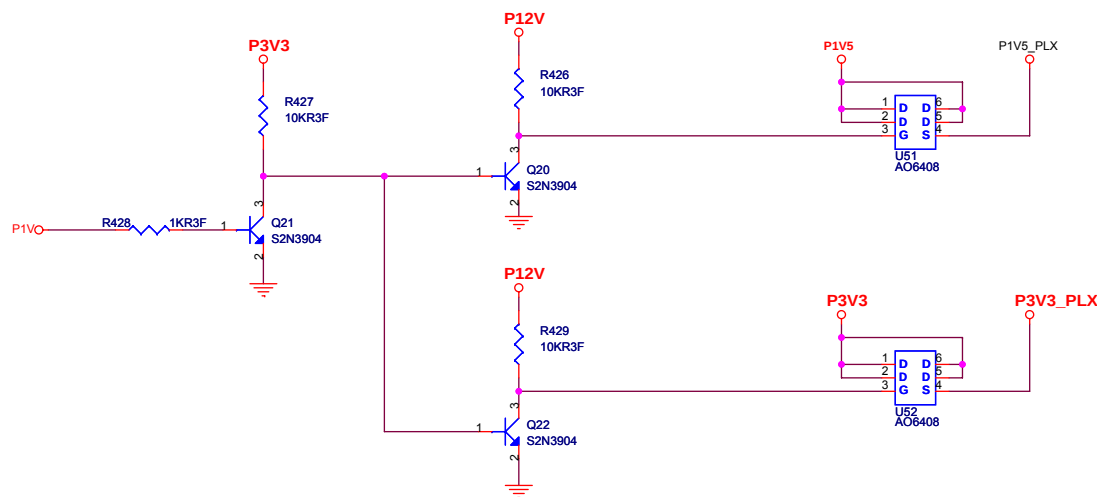
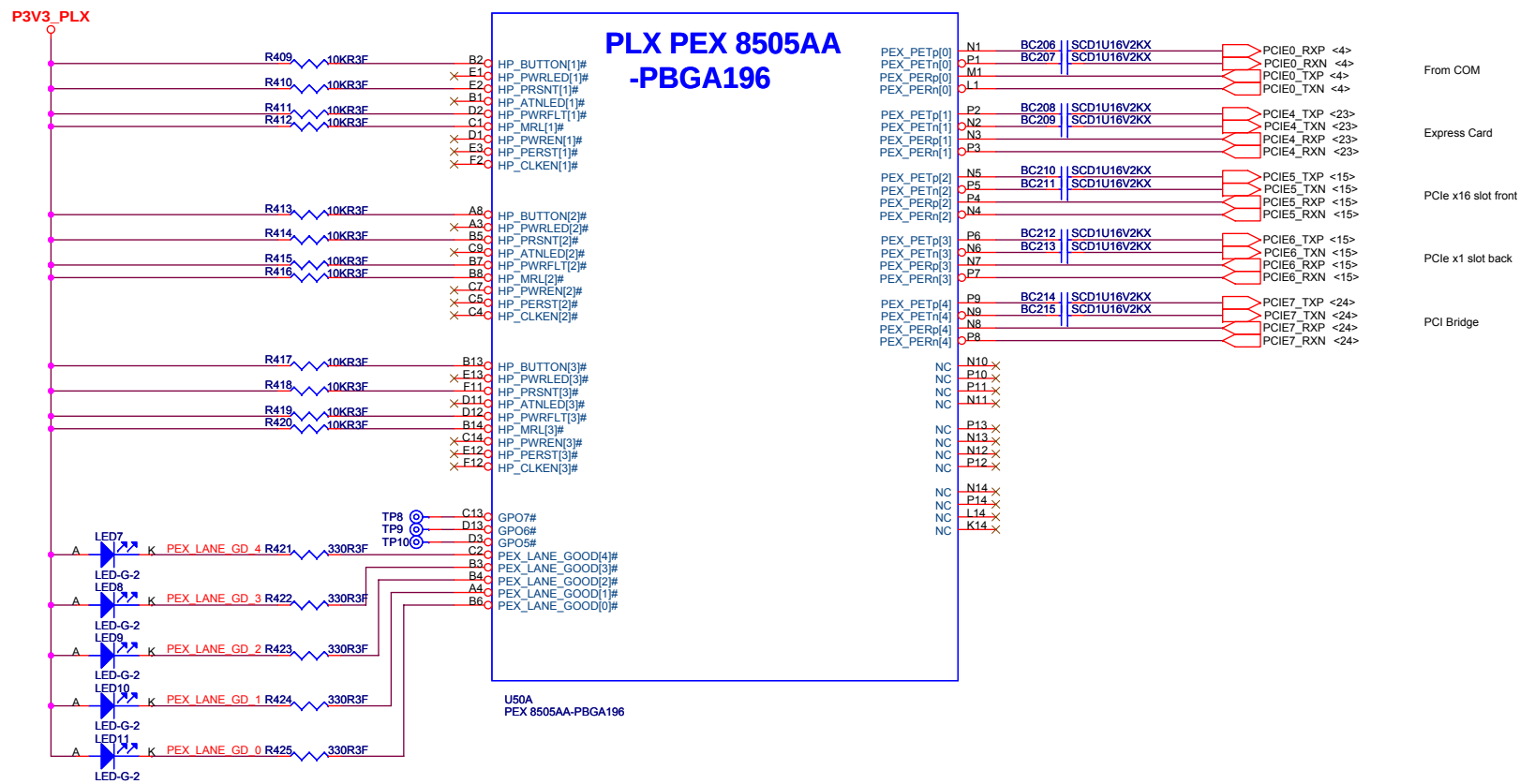


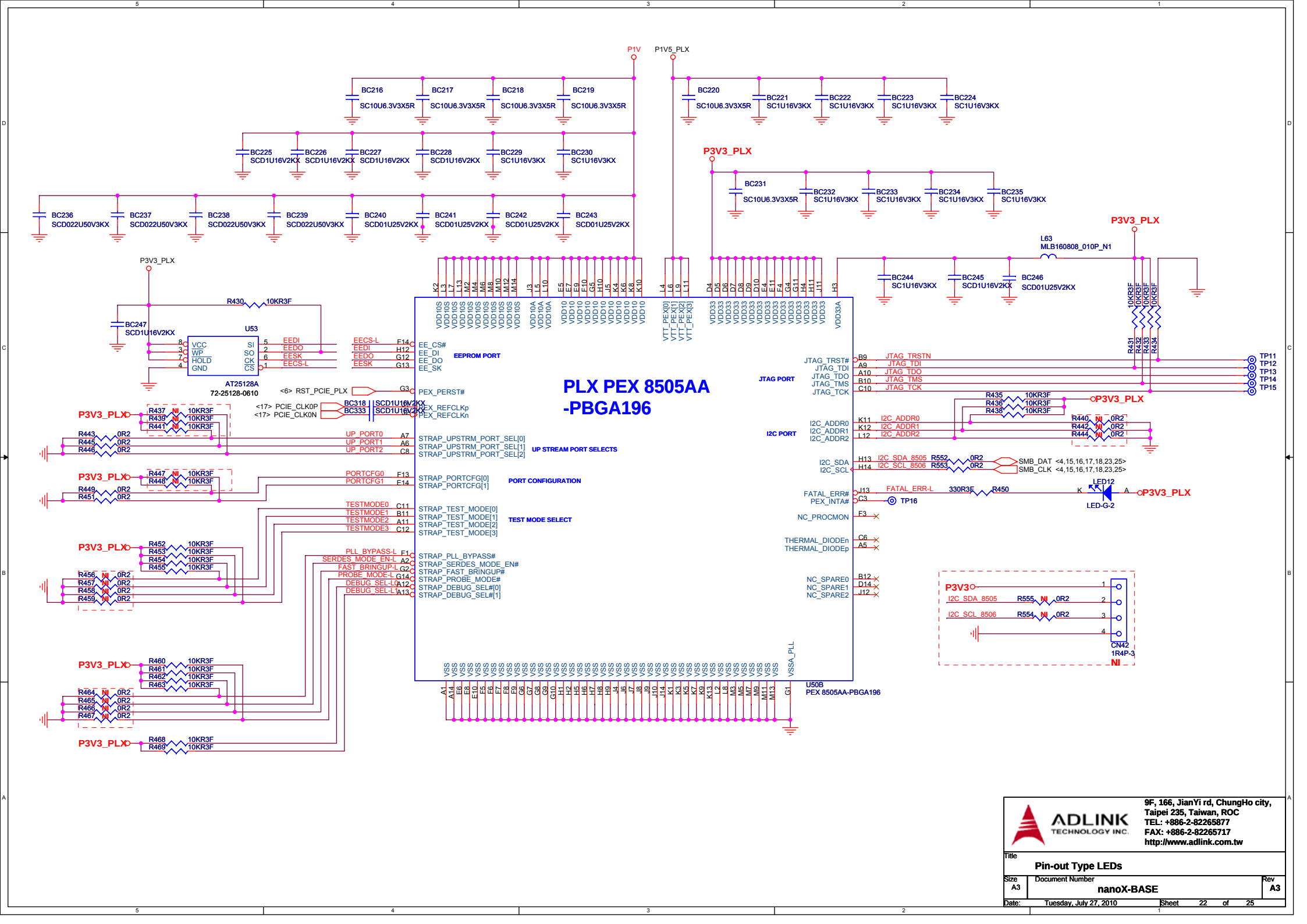
7-SEG LED (Top View)

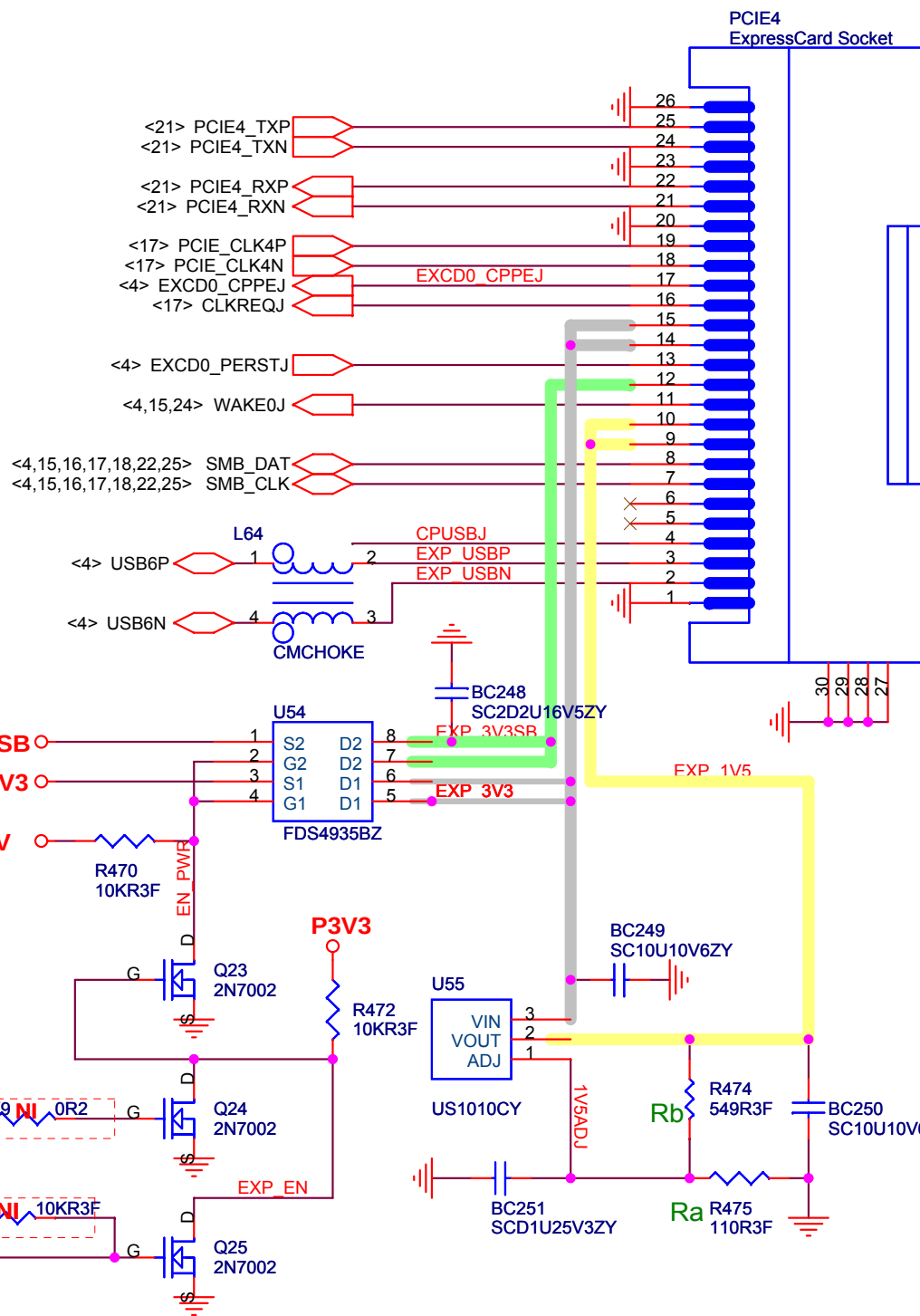


LPC CONN (FOR DEBUG)









PCB Footprint

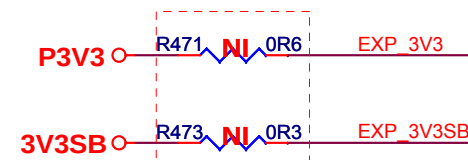
```
scnn_foxconn_1ch4110c1cx41201
scnn_tyco_1759010-1
```

Both module formats afford access to the same I/O interface performance and source power although the larger ExpressCard/54 module affords the application nominally 140% the internal volume and 160% the thermal dissipation capacity over the ExpressCard/34 module.

The sideband wake signal is only supported and implemented by ExpressCard PCI Express functions.

ExpressCard Power Supply Limits

+3.3V	1300 mA Max.
+3.3VAUX	275 mA Max.
+1.5V	650 mA Max.

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Title	Author	Year	Journal	Volume	Issue	Page
1. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	1-15
2. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	16-30
3. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	31-45
4. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	46-60
5. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	61-75
6. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	76-90
7. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	91-105
8. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	106-120
9. The Effect of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	121-135
10. The Impact of the 1997 Asian Financial Crisis on the U.S. Economy	John H. Coatsworth	1998	Journal of International Economics	50	1	136-150

ExpressCard

Size
A

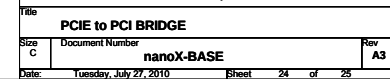
Document Number

nanoX-BASE

Rev
A3

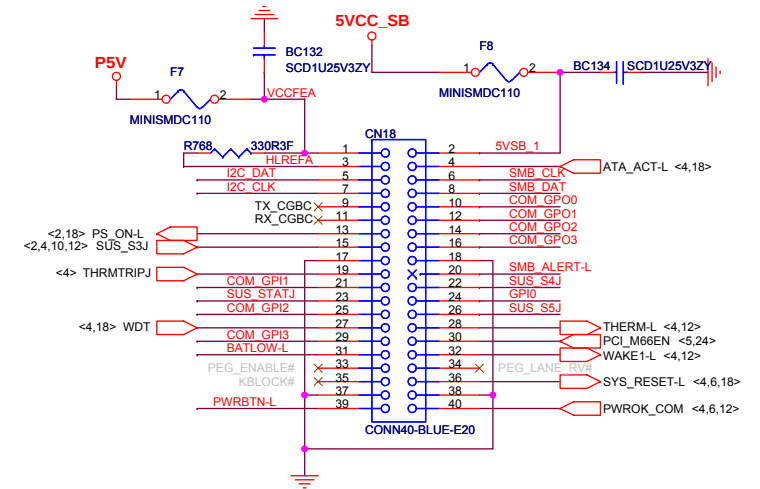
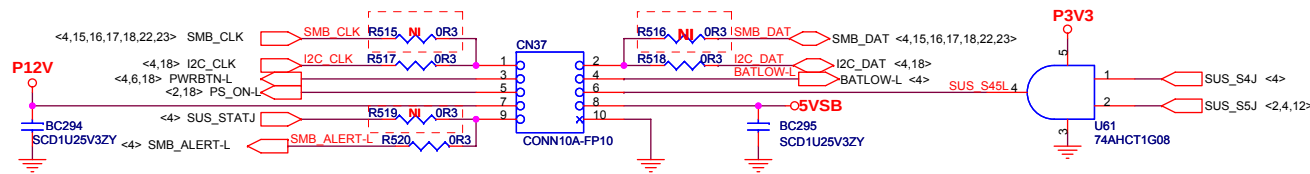
Date: Tuesday, July 27, 2010

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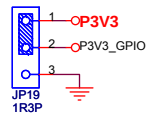
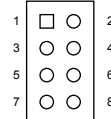
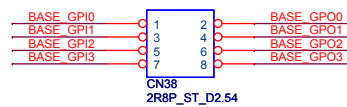


Feature Connector

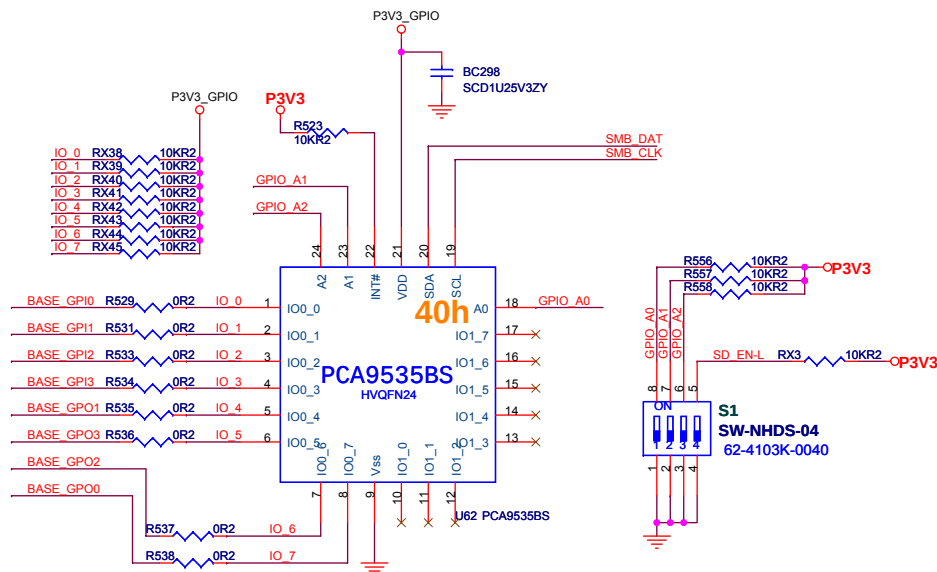
Smart Battery



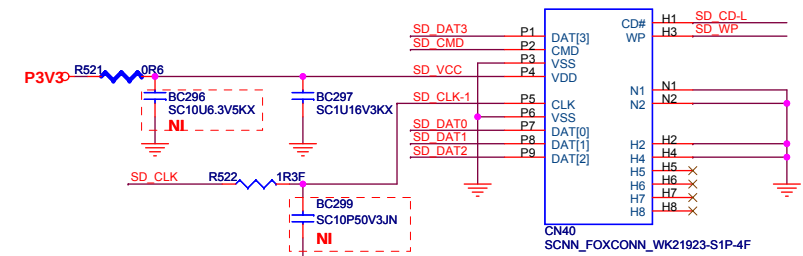
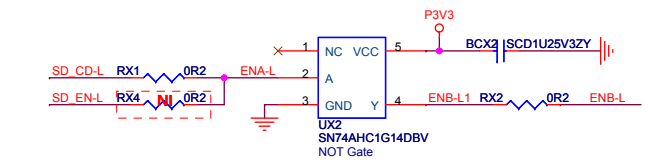
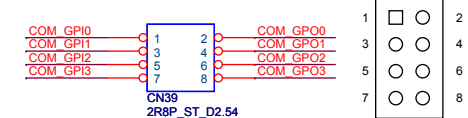
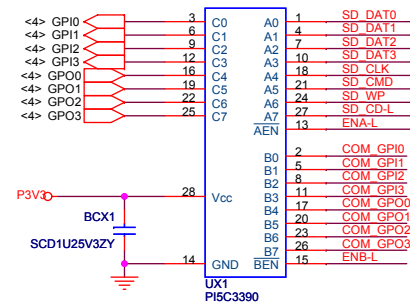
GPIO (base)



JP19 (ONBOARD GPIO)	
1-2	Enable
2-3	Disable



SD CARD/GPIO (module)



S1 (1) (GPIO address A0)	
ON	Low
OFF	High
S1 (2) (GPIO address A1)	
ON	Low
OFF	High
S1 (3) (GPIO address A2)	
ON	Low
OFF	High
S1 (4) (SD card enable)	
ON	Enable
OFF	Disable

