28 VOLT INPUT - 100 WATT

FEATURES

- -55° to +100°C operation
- 19 to 40 VDC input
- · Fully Isolated
- · Magnetic feedback
- · Fixed frequency, 600 kHz typical
- Topology Single Ended Forward
- 80 V for up to 50 ms transient protection
- · Inhibit function input and output
- · Sync function input and output
- · Output trim on single output models
- · Indefinite short circuit protection
- · Remote sense on single output models
- Up to 87% efficiency
- · Parallelable up to 228 watts



MODELS						
VDC OUTPUT						
SINGLE	DUAL					
5	±5					
12	±12					
15	±15					

DESCRIPTION

The MFLHP Series™ 28 volt DC/DC converters are rated up to 100 watts output power over a -55° to $+100^\circ$ C temperature range with a 28 VDC nominal input. On dual output models, up to 70% of the rated output power can be drawn from either the positive or negative outputs. Current sharing allows the units to be paralleled for total power of up to 270 watts. The welded, hermetically sealed package is only 3.005 x 1.505 x 0.400 inches, giving the series an overall power density of up to 67 watts per cubic inch.

DESIGN FEATURES

The MFLHP Series converters are switching regulators that use a quasi-square wave, single ended forward converter design with a constant switching frequency of 600 kHz.

Isolation between input and output circuits is provided with a transformer in the forward path and wide bandwidth magnetic coupling in the feedback control loop. The MFLHP Series uses a unique dual loop feedback technique that controls output current with an inner feedback loop and output voltage with a cascaded voltage mode feedback loop.

The additional secondary current mode feedback loop improves transient response in a manner similar to primary current mode control and allows for ease of paralleling.

Tight load regulation is achieved through a wide-bandwidth magnetic feedback circuit. The output voltage on single MFLHP models can be easily trimmed by adding an external resistor. (See Figure 1 for voltage changes with different resistor values.)

INHIBIT

The MFLHP Series converters have two TTL compatible inhibit terminals (INH1 and INH2) that can be used to disable power

conversion, resulting in a very low quiescent input current. An open collector TTL compatible low (<0.8 volts) is required between INH1 (pin 4) and Input Common (pin 2) to inhibit the converter. An open collector TTL compatible low (<0.5 volts) is required between INH2 (pin 12) and Output Common (pin 8) to inhibit the converter. The application of intermediate voltages to these pins (1.5 to 10.5 volts) should be avoided.

SYNC

Converters may be synced to an external clock (525 to 675 kHz) or to one another by using the sync in or out pins. The nominal freerun switching frequency is 600 kHz.

CURRENT SHARING AND PARALLEL OPERATION

Multiple MFLHP converters may be used in parallel to drive a common load (see Figure 2). In this mode of operation the load current is shared by two or three MFLHP converters. In current sharing mode, one MFLHP converter is designated as a master. The SLAVE pin (pin 11) of the master is left unconnected and the MSTR/INH2 pin (pin 12) of the master is connected to the SLAVE pin (pin 11) of the slave units. The units designated as slaves have the MSTR/INH2 pin (pin 12) connected to the SNS RTN pin (pin 9). Figure 2 shows the typical setup for two or three units in parallel. Note that synchronizing the units together (though shown in the figure) is not required for current sharing operation. A second slave unit may be placed in parallel with a master and slave; this requires the TRI pin (pin 3) of the master unit to be connected to the SNS RTN pin (pin 9).

When paralleled, 76% of the total combined power ratings of the MFLHP converters are available at the load. Overload and short circuit performance are not adversely affected during parallel operation.



28 VOLT INPUT - 100 WATT

OPERATING CONDITIONS AND CHARACTERISTICS

Input Voltage Range

- · 19 to 40 VDC continuous
- 80 V for 50 msec transient

Output Power

· 80 to 100 watts depending on model

Lead Soldering Temperature (10 sec per pin)

Storage Temperature Range (Case)

• -65°C to +150°C Power Dissipation (Pd)

· 20 watts

Case Operating Temperature (Tc)

- · −55 to +100°C full power
- · -55 to +135°C absolute

Derating Output Power/Current

Linearly from 100% at 100°C to 0% at 135°C

Output Voltage Temperature Coefficient

• 100 ppm/°C typical

Input to Output Capacitance

150 pF typical

Current Limit

· 115% of full load typical Isolation

• 100 megohm minimum at 500 V

Audio Rejection

50 dB typical

Conversion Frequency

- · Free run mode 600 kHz typical 550 kHz min, 650 kHz. max
- External sync range 525 to 675 kHz

Inhibit Pin Voltage (unit enabled)

• INH1 = 9 to12 V, INH2 = 6 to 9 V

SYNC IN AND INHIBIT (INH1, INH2)

Sync In (525 to 675 kHz)

- · Duty cycle 40% min, 60% max
- · Logic low 0.8 V max
- · Logic high 4.5 V min
- · Referenced to input common

Sync Out - Referenced to input common

Inhibit (INH1, INH2) TTL Open Collector

· Logic low (output disabled) Current -10 to -5 mA INH1 referenced to input common Logic low 0.8 V max INH2 referenced to output common Logic low 0.5 V max

· Logic high (output enabled) Open collector

MECHANICAL AND ENVIRONMENTAL

Size (maximum)

3.005 x 1.505 x 0.400 inches (76.33 x 38.23 x 10.16 mm) See case U for dimensions.

Weight (maximum)

100 grams

Standard or ES. See "100°C Non-QML Products-Environmental Screening (Standard and ES)" for more information.

28 VOLT INPUT - 100 WATT

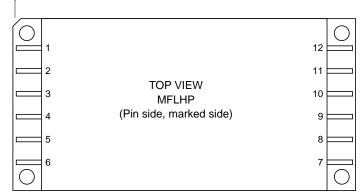
PIN OUT

Pin	Single Output	Dual Output
1	Positive Input	Positive Input
2	Input Common	Input Common
3	Triple (TRI)	Triple (TRI)
4	Inhibit 1 (INH1)	Inhibit 1 (INH1)
5	Sync Out	Sync Out
6	Sync In	Sync In
7	Positive Output	Positive Output
8	Output Common	Output Common
9	Sense Return	Negative Output
10	Positive Sense	No connection
11	Slave	Slave
12	Master / Inhibit 2	Master / Inhibit 2
	(MSTR/INH2)	(MSTR/INH2)

PINS NOT IN USE

TR1	Leave unconnected					
Master	Leave unconnected					
Slave	Leave unconnected					
Sync in	Connect to input common					
Inhibit (INH1)	Leave unconnected					
Inhibit (INH2)	Leave unconnected					
Sync Out	Leave unconnected					
Sense Lines	Must be connected to					
	appropriate outputs					

Angled corner indicates pin one.

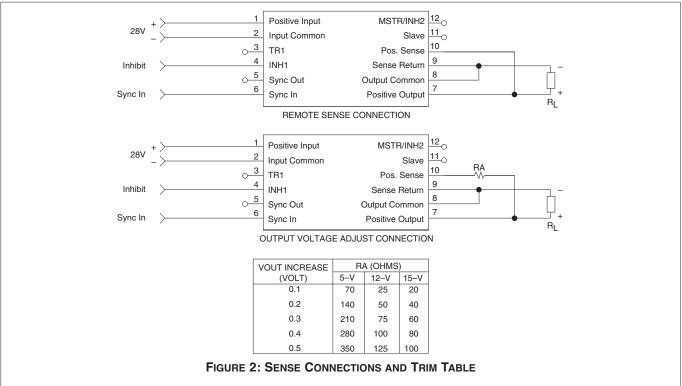


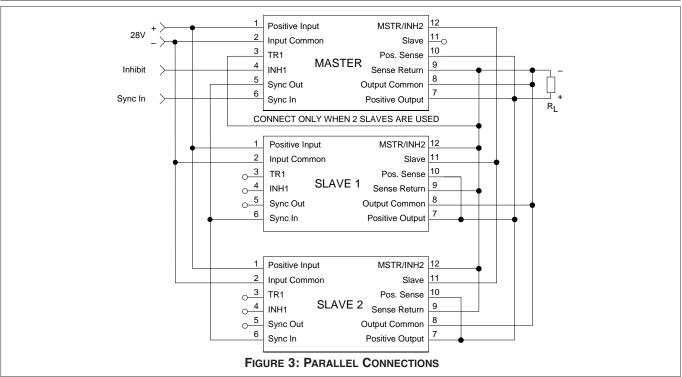
See case U for dimensions.

FIGURE 1: PIN OUT

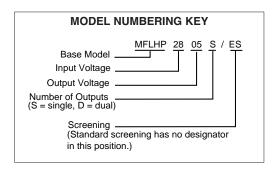
28 VOLT INPUT - 100 WATT

SINGLE OUTPUT MODELS CONNECTION DIAGRAMS - SENSE AND PARALLEL





28 VOLT INPUT - 100 WATT



Model Selection									
MFLHP28 Base model	Vout value	number of outputs	 case option	 screening					
Choose one from e	ach of the fo	llowing rows							
Vout value	for single	es and duals: 5, 12, 15							
Number of output	s S (single	e) or D (dual)							
Case option	standard	(case U)							
Screening	standard	screening, leave blank	/ES (ES screening)						

28 VOLT INPUT - 100 WATT

Electrical Characteristics: -55°C to +100°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.

SINGLE OUTPUT MODELS		MFLHP2805S		MFLHP2812S			MFL	_HP28			
PARAMETER	CONDITION	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	25°C	4.95	5.00	5.05	11.88	12.00	12.12	14.85	15.00	15.15	VDC
OUTPUT CURRENT	V _{IN} = 19 to 40 VDC	0	_	16	0	_	7.5	0	_	6.67	A
OUTPUT POWER	V _{IN} = 19 to 40 VDC	0	_	80	0	_	90	0	_	100	W
OUTPUT RIPPLE	Tc = 25°C	_	15	35	T -	30	75	_	30	85	
VOLTAGE 10 k - 2 MHz	$Tc = -55^{\circ}C \text{ to } +100^{\circ}C$	_	30	75	_	45	125	_	45	150	mV p-p
LINE REGULATION	V _{IN} = 19 to 40 VDC	_	0	20	_	0	20	_	0	20	mV
LOAD REGULATION	NO LOAD TO FULL	_	0	20	_	0	20	_	0	20	mV
INPUT VOLTAGE	CONTINUOUS	19	28	40	19	28	40	19	28	40	VDC
NO LOAD TO FULL	TRANSIENT ² 50 ms ¹	_	_	80	_	_	80	_	_	80	V
INPUT CURRENT	NO LOAD	_	70	120	_	50	80	_	50	80	mA
	INHIBITED - INH1	_	9	14	_	9	14	_	9	14	mA
	INHIBITED - INH2	_	35	70	_	35	70	_	35	70	TIIA
INPUT RIPPLE											
CURRENT	10 kHz - 10 MHz	_	15	50	_	15	50	_	15	50	mA pp
EFFICIENCY	Tc = 25°C	78	80	_	83	86	_	84	87	_	%
LOAD FAULT	POWER DISSIPATION										
$Tc = 25^{\circ}C$	SHORT CIRCUIT		15	20	_	15	20	_	15	20	W
	RECOVERY ¹	_	1.5	4	_	1.5	4	_	1.5	4	ms
STEP LOAD RESP.	50% - 100% - 50%										
	TRANSIENT	-	350	450	_	450	700	_	450	700	mV pk
	RECOVERY ³	_	1.5	3.0	_	1.5	3.0	_	1.5	3.0	ms
STEP LINE RESP.	19 – 40 – 19 VDC										
	TRANSIENT ^{1, 4}	_	250	400	_	250	400	_	250	400	mV pk
	RECOVERY ^{1, 3}	_	200	300	_	200	300	_	200	300	μs
START-UP	DELAY	_	3.5	6	_	3.5	6	_	3.5	6	ms
	OVERSHOOT ¹	-	0	25	-	0	50	_	0	50	mV pk
	1										I

Notes

- 1. Guaranteed by design, not tested.
- Unit will shut down above approximately 45V but will be undamaged and will restart when voltage drops into normal range.
- 3. Recovery time is measured from application of the transient to point at which Vout is within 1% of final value.

5. Transition time \geq 10 μ s.

28 VOLT INPUT - 100 WATT

Electrical Characteristics: -50°C to +100°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.

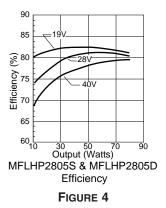
DUAL OUTPUT MODELS		MFLHP2805D		MFLHP2812D			MFLHP2815D				
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	+V _{OUT}	4.95	5.00	5.05	11.88	12.00	12.12	14.85	15.00	15.15	VDC
$Tc = 25^{\circ}C$	-V _{OUT}	4.92	5.00	5.08	11.82	12.00	12.18	14.77	15.00	15.23	VDC
OUTPUT CURRENT ²	EACH OUTPUT	0	_	11.2	0	_	5.3	0	_	4.67	
V _{IN} = 19 TO 40 VDC	TOTAL	0	_	16.0	0	_	7.5	0	_	6.67	A
OUTPUT POWER	EACH OUTPUT	0	_	56	0	_	63	0	_	70	w
V_{IN} = 19 TO 40 VDC	TOTAL	0	_	80	0	_	90	0	_	100	1 VV
OUTPUT RIPPLE	10 kHz - 2 MHz										
VOLTAGE	+V _{OUT}	_	25	125	_	50	150	_	50	200	.,
	-V _{OUT}	_	25	125	_	50	150	_	50	200	mV p-p
LINE REGULATION	+V _{OUT}	_	0	50	_	0	50	_	0	50	mV
V _{IN} = 19 TO 40 VDC	-V _{OUT}	_	25	100	_	25	100	_	25	100	IIIV
LOAD REGULATION	+V _{OUT}	_	0	50	_	10	100	_	10	100	.,
NO LOAD TO FULL	-V _{OUT}	_	25	100	_	50	200	_	50	200	mV
CROSS REGULATION	SEE NOTE 3	_	6	8	-	2	4	_	2	4	%
Tc = 25°C	SEE NOTE 4	_	7	8	_	2	4	_	2	4	70
INPUT VOLTAGE	CONTINUOUS	19	28	40	19	28	40	19	28	40	VDC
NO LOAD TO FULL	TRANSIENT ^{1, 5} 50 ms	0	_	80	0	_	80	0	_	80	V
INPUT CURRENT	NO LOAD	_	50	120	_	50	100	_	550	100	mA
$Tc = 25^{\circ}C$	INHIBITED - INH1	_	9	14	_	9	14	_	9	14	A
	INHIBITED - INH2	_	35	70	_	35	70	_	35	70	mA
INPUT RIPPLE											
CURRENT	10 kHz - 10 MHz	_	15	50	_	15	50	_	15	50	mA p-p
EFFICIENCY 25°C Tc	BALANCED LOAD	78	80		83	86		84	87	_	%
LOAD FAULT	POWER DISSIPATION										
$Tc = 25^{\circ}C$	SHORT CIRCUIT	_	15	20	_	15	20	_	15	20	W
	RECOVERY ¹	_	1.5	4.0	_	1.5	4.0	_	1.5	4.0	ms
STEP LOAD RESP.	50 %-100%- 50% LOAD										
± V _{OUT}	TRANSIENT	_	350	450	_	450	700	-	450	700	mV pk
	RECOVERY ⁶	_	1.5	3.0	_	1.5	3.0	_	1.5	3.0	ms
STEP LINE RESP.	19 – 40 – 16 V _{IN}										
± V _{OUT}	TRANSIENT ^{1, 7}	_	250	400	_	250	400	_	250	400	mV pk
	RECOVERY ^{1, 6}	_	200	300	_	200	300	_	200	300	μs
START-UP	DELAY	_	3.5	6	_	3.5	6	_	3.5	6	ms
	OVERSHOOT ¹	_	0	25	_	0	50	_	0	50	mV p

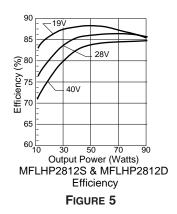
Notes:

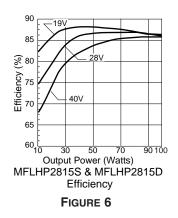
- 1. Guaranteed by design, not tested.
- 2. Up to 70% of the total output power is available from either output providing the opposite output is simultaneously carrying 30% of the total power.
- 3. Effect on negative Vout from 50%/50% loads to 30%/70% or 70%/30% loads
- 4. Effect on negative Vout from 50%/50% loads to 10% then 50% load on negative Vout.
- Unit will shut down above approximately 45V but will be undamaged and will restart when voltage drops into normal range.
- 6. Recovery time is measured from application of the transient to point at which Vout is within 1% of final value.
- 7. Transition time \geq 10 μ s.

28 VOLT INPUT - 100 WATT

Typical Performance Curves: 25°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.







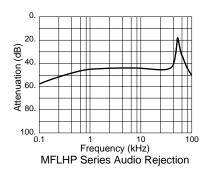
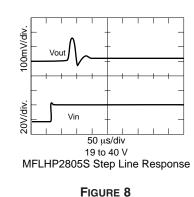
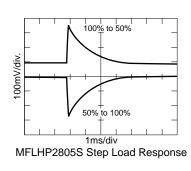
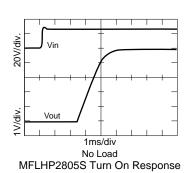
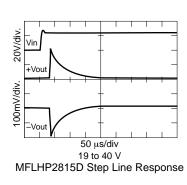


FIGURE 7









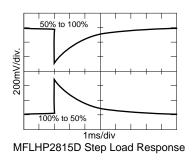


FIGURE 9

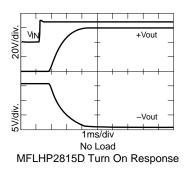
FIGURE 10

FIGURE 11

FIGURE 12

28 VOLT INPUT - 100 WATT

Typical Performance Curves: 25°C Tc, 28 VDC Vin, 100% load, free run, unless otherwise specified.





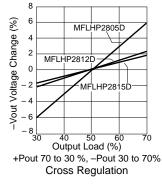


FIGURE 14

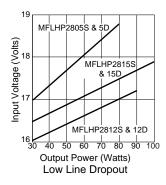


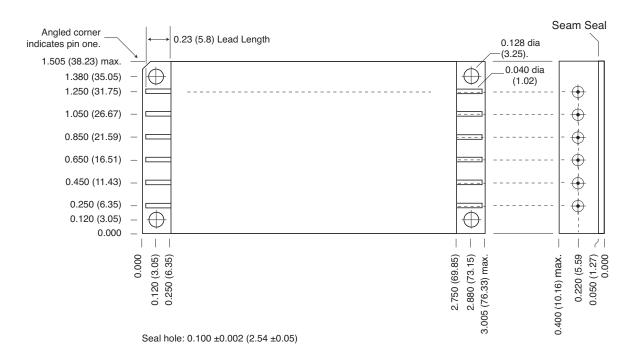
FIGURE 15

28 VOLT INPUT - 100 WATT

TOP VIEW CASE U

Flanged case, short-leaded

*Does not require designator in Case Option position of model number.



Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

CAUTION

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin

Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold; compression glass seal

Case U, Rev C, 20060302

Please refer to the numerical dimensions for accuracy. All information is believed to be accurate, but no responsibility is assumed for errors or omissions. Interpoint reserves the right to make changes in products or specifications without notice.

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FIGURE 16: CASE U

28 VOLT INPUT - 100 WATT

100°C Non-QML Products—Environmental Screening (Standard and ES)

TEST STANDARD	100°C STANDARD non-QML ¹	100°C /ES non-QML ¹
Pre-cap Inspection		
Method 2017, 2032	yes	yes
Temperature Cycle (10 times)		
Method 1010, Cond. B, -55°C to 125°C, ambient	no	yes
Constant Acceleration Method 2001, 500g	no	VAS
Method 2001, 500g	110	yes
Burn-In 96 hours, typical case temperature at 100°C case ^{2, 3}	no	yes
or means, typical sace temperature at 100 0 sace		you
Final Electrical Test MIL-PRF-38534, Group A		
Subgroups 1 and 4: +25°C case	yes	yes
Hermeticity Test		
Fine Leak, Method 1014, Cond. A	no	yes
Gross Leak, Method 1014, Cond. C	no	yes
Gross Leak, Dip (1 x 10 ⁻³)	yes	no
Final Visual Inspection		
Method 2009	yes	yes

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

Notes:

- 1. Non-QML products do not meet all of the requirements of MIL-PRF-38534
- 2. May vary depending on family and model, see individual datasheets for specific information
- 3. Burn-in may be still air with an ambient temperature designed to bring the case temperature to 100°C

